The syntax and semantics of timed $\mu$CRL$\$ 

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Abstract

We define a specification language called ‘timed $\mu$CRL’. This language is designed to describe communicating processes employing data and time. Timed $\mu$CRL is the successor of $\mu$CRL [17]. It differs in two main aspects. It is possible to make explicit reference to time using a new ‘at’ operator; $pt$ is the process $p$ where the first action must take place at time $t$. Furthermore, a distinction has been made between constructors and functions in the datatypes. Care has been taken that every $\mu$CRL specification is also a correct timed $\mu$CRL specification with exactly the same meaning.

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Keywords and Phrases: Specification Language, Abstract Data Types, Process Algebra, Operational Semantics, Real Time

1 Introduction

The language $\mu$CRL (micro Common Representation Language) has been defined to describe interacting processes that rely on data [17]. The major design objectives for $\mu$CRL were that

- $\mu$CRL had to be so expressive that ‘real life systems’, generally consisting of a set of interacting programs, could be described.

- $\mu$CRL had to be so simple and clear that it was suitable to form a basis for mathematical analysis.

- the definition of $\mu$CRL had to be sufficiently precise to allow for the independent construction of computer tools for $\mu$CRL capable in assisting in the actual development of systems.

We are confident to say that $\mu$CRL has indeed met all these intended purposes (see for example [5, 7, 8, 9, 15, 16, 18, 19, 21, 24, 28, 29, 33]). However, the need was felt to be able to deal with a more explicit notion of time. Moreover, the datatypes needed to be adapted to overcome some shortcomings, which are independent of the extension with time. Timed $\mu$CRL is the extension of $\mu$CRL in both respects. This extension is carried out in such a way that every $\mu$CRL specification is also a timed $\mu$CRL specification.
The real time extension

The language μCRL primarily describes the potential ordering of actions that processes can perform, which is called the behaviour of a process. It is easy to express that an action \( a \) must happen before an action \( b \), and that action \( c \) happens in parallel with these. There is however no explicit reference to time. E.g. μCRL cannot express that action \( b \) must happen within five seconds after \( a \), or if nothing is happening for 10 seconds, an alarm action is issued.

However, time is used explicitly in almost any computerized system. Computers rely on notions such as time-outs, scheduling and interrupts. Hardware and software clocks are common as are repeating time-based tasks. Real-time systems even add another dimension. These are required to perform certain tasks within predetermined time intervals. For such systems time is not only important for the internal behaviour, but time is also an important notion when it comes to the interaction with the environment.

Therefore, we have extended μCRL to handle time.

The major changes and major design considerations for the extension with time are given below.

- A specification in timed μCRL that makes no reference to time should look exactly the same as a ‘classical’ μCRL specification. Moreover, the intuitive meaning of such a specification should be equal in both languages. Formally, both semantics of this specification differ, as the semantics of timed μCRL explicitly refers to time, whereas the semantics of classical μCRL does not mention time at all.

- The extension with time should be natural and concise, fitting in the style of description of μCRL.

We choose to extend μCRL with two operators, namely \( x.t \) (pronounce \( x \) at \( t \)), expressing that the first action of \( x \) must take place at time \( t \), and \( x \preceq y \) (pronounce \( x \) before \( y \)) behaving as the process \( x \) that starts before \( y \) must have performed an action.

We choose to let each term of sort time be an absolute reference to a moment in time, and not a reference relative to previous actions. We only selected one option, because descriptions with relative and absolute notions of time can easily be converted to each other. Terms denoting time are of sort \( \text{Time} \), which must be declared in the datatype whenever the ‘at’ operator is used. This sort has at least two functions, namely 0, which is the starting point in time, and \( \preceq \) which is a total ordering on time. We leave it open whether \( \text{Time} \) is a discrete or dense domain, to be determined for each particular specification. Although \( x.t \) only refers to a single point in time, the sum operator \( \Sigma_{t:\text{Time}} \) in combination with the conditionals allows expression of any time interval.

- Timed μCRL should be as suitable for analysis and verification purposes as μCRL turned out to be. We did do a number of exercises with timed verifications. This has resulted in allowing simultaneous, subsequent actions. Concretely, it is allowed to write \( a!1.b!1 \), meaning that \( a \) takes place at time 1, followed by \( b \) taking place at time 1. Here we clearly sacrificed naturality of the language in favour of ease of use (as in \([2, 23]\)). The most important reason to allow simultaneity is the elimination of the parallel operator in expressions of the form \( a!1 \parallel b!1 \). Using linear process operators \([6]\), it is generally possible to eliminate the parallel operator without exponential blowup in this way. One of the alternatives that we investigated, is the use of multi-actions, e.g. \( (a-b)!1 \) \([1]\), but this gives rise to an exponential number of multi-actions, when expanding \( n \) parallel processes, seriously hampering any verification effort.

Adaptation of the datatypes

In untimed μCRL, datatypes are defined by declaring a number of functions using the keyword \texttt{func} and by specifying via equations which terms must be considered equal. The declared functions are considered to be constructors of the datatype leading to the problems described below. We have added the possibility in timed μCRL to declare non-structor functions using the keyword \texttt{map}.
1 INTRODUCTION

• When specifying a data type with a small number of typical constructors, e.g. the natural numbers with zero and the successor function, and additionally some non constructor functions, such as addition, one is forced in μCRL to declare the non constructor functions as a constructor. This has as a consequence that whenever elementary properties about, say, the natural numbers are proven, induction on zero, the successor and addition has to be performed, or it has to be proven that addition can be eliminated from each term denoting a natural number. If addition is declared using map in timed μCRL it has the status of a non constructor function, and it is not necessary to incorporate it in the induction principle.

• It is not possible to declare an arbitrary constant in μCRL without specifying its value or introducing new elements. E.g. if a natural number is declared in μCRL by ‘func n’, without specifying its value, it is taken as a new base natural number and succ(n), succ(succ(n)), etc. are also new natural numbers. In timed μCRL this is easily solved by declaring ‘map n’, which must be equal to some term denoted using the constructors 0 and succ.

• In μCRL it is impossible to declare a domain without determining the elements occurring in it. For instance declaring an arbitrary data domain $D$ without any function with $D$ as target domain would be the empty domain, which is explicitly forbidden in μCRL. In case a single element $\text{func } d_0: \rightarrow D$ is declared this means that $D_0$ has exactly one single element $d_0$, and no other elements. However, often the intention of $\text{func } d_0: \rightarrow D$ is that $D$ is an arbitrary domain with at least a default element $d_0$. In timed μCRL this can correctly be expressed by ‘map $d_0: \rightarrow D$’.

Changes in the operational semantics

The operational semantics of timed μCRL differs in two major aspects from the operational semantics of μCRL. In the first place every state has been extended with time. So, each state consists of a process and a moment in time. Secondly, transitions between states are labeled with actions with arguments of the form $a(t_1, \ldots, t_n)$ where $t_i$ is a data term. As the introduction of map allows to have domains in the model of which not all elements can be denoted by terms, we introduce ‘fresh’ constants to the signature to denote these model elements.

Minor differences

The following minor alterations have been added in timed μCRL. Note that they have been defined such that every μCRL specification is still a correct timed μCRL specification.

In timed μCRL the possibility has been added to denote the initial state of a process by denoting $\text{init } p$ where $p$ is a closed process expression.

The properties of datatypes are described by equations. When defining μCRL it was assumed that these equations should be rewriting rules, as rewriting was the only conceived way of proving properties using the equations. This means that the left hand side of a rewriting rule was not allowed to be a single variable and the variables in the right hand side of an equations should also occur in the left hand side. In timed μCRL this is not required anymore.

It has been tried to make the definition of timed μCRL more accessible than the definition of μCRL. Therefore, the main text consists of an introduction to timed μCRL explaining how to specify in timed μCRL and how to carry out some elementary calculations. In Appendices A, B, C and D the syntax, static semantics, models of the datatype and the operational semantics of timed μCRL are defined, respectively.

Acknowledgements. I thank all persons who assisted in developing μCRL throughout the years. For timed μCRL I have benefited a lot from comments from Jos Baeten, Jan Bergstra, Bas Luttik, Alban Ponse, Michel Reniers, Jan Springintveld, Jan-Joris Vereijken and all people within COST 247, especially those developing Extended LOTOS [25], which also includes time.
2 THE LANGUAGE

<table>
<thead>
<tr>
<th>variable</th>
<th>range</th>
</tr>
</thead>
<tbody>
<tr>
<td>(x, y, z)</td>
<td>processes</td>
</tr>
<tr>
<td>(X, Y)</td>
<td>functions from data to processes</td>
</tr>
<tr>
<td>(D)</td>
<td>data sort</td>
</tr>
<tr>
<td>(d, e)</td>
<td>data terms of arbitrary sort</td>
</tr>
<tr>
<td>(b)</td>
<td>data terms of sort (\text{Bool})</td>
</tr>
<tr>
<td>(t, t', t_1, t_2, t_3)</td>
<td>data terms of sort (\text{Time})</td>
</tr>
<tr>
<td>(c, c')</td>
<td>actions with one argument* ((a(d)), \tau \text{ or } \delta)</td>
</tr>
<tr>
<td>(a, a')</td>
<td>action names</td>
</tr>
</tbody>
</table>

*in the axioms we assume that each action has exactly one argument.

Table 1: Conventions in the tables with axioms

2 The language

We provide an introduction into timed \(\mu\)CRL. Note that we use \LaTeX\ type setting features at will. The only precise syntax for timed \(\mu\)CRL is given for plain text specifications (see Table 7 and Appendix A). This syntax is meant for specifications intended to be computer processed and for specifications of which their syntactically appearance must be unambiguously parsable. In expository texts we try to optimize readability.

2.1 Axioms

Before commencing to explain the language we spend a few words on the tables with axioms. For all operators in the language we provide characterizing axioms following the process algebraic tradition. Basically, they assist in interpreting the language correctly. The semantics of the language is given in terms of a data algebra and an operational semantics (see appendices C and D) and provides a rather operational understanding. The axioms provide a more syntactical perspective. Secondly, these laws are useful, as they explain why in some cases brackets may be omitted. For instance, it is allowed to write \(a + b + c\), because, due to A2 in Table 2, it does not matter how brackets are put. The third reason for having these axioms is that they form the elementary basis for axiomatic reasoning about processes. We give an extensive set of examples of this in section 4. The conventions regarding the variables in the tables with axioms are summarized in Table 1.

2.2 Abstract data types

Processes in \(\mu\)CRL generally exchange data. In timed \(\mu\)CRL there is a simple, yet powerful mechanism to specify the data. We use (equational) abstract data types with an explicit distinction between constructor functions and ‘normal’ functions. The advantage of having such a simple language is that it can easily be explained and formally defined. Moreover, all properties of a datatype must be explicitly denoted, and henceforth it is clear which assumptions can be used when proving properties about data or processes. A disadvantage is of course that even the simplest datatypes must be specified each time, and that there are no high level constructs that allow compact specification of complex datatypes.

Each data type is declared using the keyword **sort**. Therefore, a data type is also called a data sort. Each declared sort represents a non-empty set of data elements. Declaring the sort of the booleans is simply done by:

```plaintext
sort  Bool
```
Because booleans are used in the if-then-else construct in the process language, the sort $\texttt{Bool}$ must be declared in every timed $\mu\text{CRL}$ specification.

Elements of a data type are declared by using the keywords $\texttt{func}$ and $\texttt{map}$. Using $\texttt{func}$ one can declare all elements in a datatype defining the structure of the datatype. E.g. by

\begin{verbatim}
sort $\texttt{Bool}$
func $\texttt{t}, \texttt{f}: \to \texttt{Bool}$
\end{verbatim}

one declares that $\texttt{t}$ (true) and $\texttt{f}$ (false) are the only elements of sort $\texttt{Bool}$. We say that $\texttt{t}$ and $\texttt{f}$ are the constructors of sort $\texttt{Bool}$. It is also obligatory, that $\texttt{t}$ and $\texttt{f}$ are declared in every specification. Moreover, it is assumed that $\texttt{t}$ and $\texttt{f}$ are the two (different) elements in $\texttt{Bool}$. This is expressed in axioms $\text{Bool1}$ and $\text{Bool2}$ in Table 2. In axiom $\text{Bool2}$ and elsewhere we use a variable $b$ that can only be instantiated with data terms of sort $\texttt{Bool}$. If in a specification $\texttt{t}$ and $\texttt{f}$ can be proven equal, for instance if the specification contains an equation $\texttt{t} = \texttt{f}$, we say that the specification is inconsistent and it looses any reasonable meaning.

It is now easy to declare the natural numbers, in the logician’s style, using the constructors zero $\texttt{0}$ and successor $\texttt{S}$.

\begin{verbatim}
sort $\texttt{Bool}, \texttt{N}$
func $\texttt{t}, \texttt{f}: \to \texttt{Bool}$
  $\texttt{0}: \to \texttt{N}$
  $\texttt{S}: \texttt{N} \to \texttt{N}$
\end{verbatim}

This says that each natural number can be written as $\texttt{0}$ or as the application of a finite number of successors to $\texttt{0}$.

If a sort $D$ is declared without any constructor with target sort $D$, then it is assumed that $D$ may be arbitrarily large. In particular $D$ may contain elements that cannot be denoted by terms. This can be extremely useful, for instance when defining a data transfer protocol, that can transfer data elements from an arbitrary domain $D$. In such a case it suffices to declare in timed $\mu\text{CRL}$:

\begin{verbatim}
sort $D$
\end{verbatim}

Note that in $\mu\text{CRL}$ this was not allowed. According to its semantics this would yield an empty sort, as there were no constructors, and this was explicitly forbidden.

The keyword $\texttt{map}$ is used to declare additional functions for a domain of which the structure is already given. For instance declaring a function $\land$ on the booleans, or declaring the $+$ on natural numbers can be done by adding the following lines to a specification in which $\texttt{N}$ and $\texttt{Bool}$ are already declared:

\begin{verbatim}
map $\land: \texttt{Bool} \times \texttt{Bool} \to \texttt{Bool}$
  $+: \texttt{N} \times \texttt{N} \to \texttt{N}$
\end{verbatim}

By adding plain equations, of the form $\texttt{term} = \texttt{term}$, assumptions about the functions can be added. Note that we use the keyword $\texttt{rew}$ for equations confusingly suggesting that the equations must be rewrite rules. This is inherited from $\mu\text{CRL}$, where these equations were indeed intended to be rewrite rules. For the two functions declared above, we could add the equations:

\begin{verbatim}
var  $x: \texttt{Bool} \ n, n': \texttt{N}$
rew  $x \land t = x$
     $x \land f = f$
     $n + 0 = n$
     $n + S(n') = S(n + n')$
\end{verbatim}
Note that before each group of the equations starting with the keyword `rew` we must declare the variables that are used.

Note also that although the machine readable syntax of timed $\mu$CRL in section A only uses prefix functions, we use these infix, if we believe that this increases readability. Moreover, we use common mathematical symbols such as $\land$ and $\lor$, which is also not allowed by the syntax of timed $\mu$CRL, for the same reason.

Ultimately, we remark that the equations for a datatype can be used in proving properties about data and process terms (see section 4.1).

If a specification refers to time, using the ‘at’ operator, it is obligatory to declare a domain `Time` as follows:

```plaintext
sort Time
map 0:→ Time
     ≤ :Time × Time → Bool
```

Instead of `map`, it is possible to use `func` for $0$. The functions $0$ and $\leq$ must satisfy the properties mentioned in Table 3, saying that $\leq$ is a total ordering and $0$ is the smallest element. In order to formulate these properties, we have defined some auxiliary functions in this table.

Besides these requirement one is free to specify the kind of time one requires. This is done to accomodate those preferring discrete time, and others who prefer a notion of dense time. It is possible to give `Time` any structure, as long as it satisfies the axioms in Table 3. One can for instance define that `Time` has only a finite number of elements, or one can define an ordinal like structure on it.

Discrete time can be specified as follows (omitting the specification of `Bool`):

```plaintext
sort Time, Bool
func 0:→ Time
     next:Time → Time
     t,f:→ Bool
map ≤ :Time × Time → Bool
var t,t’:Time
rew next(t) ≤ 0 = f
     next(t) ≤ next(t’) = t ≤ t’
```

Note that we do not explicitly state that $0 \leq t = t$, because this is already in Table 3.

Functions may be overloaded, as long as every term has a unique sort. This means that the name of the function together with the sort of its arguments must be unique. E.g, it is possible to declare $\max:\mathbb{N} \times \mathbb{N} \rightarrow \mathbb{N}$ and $\max:\text{Bool} \times \text{Bool} \rightarrow \text{Bool}$, but is not allowed to declare a function $f:\text{Bool} \rightarrow \text{Bool}$ and $f:\text{Bool} \rightarrow \mathbb{N}$. Actually, the overloading rule holds in general in timed $\mu$CRL such that every term is either an action, process name or a data term, and if it is a data term, it has a unique sort.

When declaring a sort $D$, it is required that it may not be empty. Therefore, the following declaration is invalid.

```plaintext
sort D
func f:D → D
```

It declares that $D$ is a domain in which all the terms have the form $f(f(f(...)))$, i.e. an infinite number of applications of $f$. Such terms do not exist, and therefore $D$ must be empty. This problem can also occur with more than one sort. E.g. sorts $D$ and $E$ with constructors from $D$ to $E$ and $E$ to $D$. Fortunately, it is easy to detect such problems and therefore it is a static semantic constraint that such empty sorts may not occur (see Appendix B).
2.3 Actions

Actions are abstract representations of events in the real world that is described. For instance sending the number 3 can be described by send(3) and boiling food can be described by boil(food) where 3 and food are terms belonging to some sort. An action consists of a name possibly followed by one or more data terms within brackets. Actions are declared using the keyword act followed by a name and the sorts of the data that it can exchange. Below, we declare the action name timeout over which no data is transferred, and an action a that is used to transfer booleans, and pairs of natural numbers and data elements of sort $D$.

\[
\text{act } \text{timeout} \\
a: \text{Bool} \\
a: \mathbb{N} \times D
\]

In the tables with axioms we use the letter $a$ for an action label, and we give each action a single argument, although in $\mu$CRL these actions may have zero or more than one argument. The letter $c$ is used for actions with arguments, and for the constants $\tau$ and $\delta$.

2.4 Occurrences of processes

Processes are expressions built upon actions indicating when a process may engage in certain events. In other words, a process represents the potential behaviour of a certain system.

In a timed $\mu$CRL specification processes appear at two places. First, there can be a single occurrence of an initial declaration, of the form

\[
\text{init } p
\]

where $p$ is a process expression indicating the initial behaviour of the system that is described. The init section may be omitted, in which case the initial behaviour of the system is left unspecified.

The other place where process expressions may occur are in the right hand side of process declarations, which have the form:

\[
\text{proc } X(x_1:s_1, \ldots, x_n:s_n) = p
\]

Here $X$ is the process name, the $x_i$ are distinct variables, not clashing with the name of a constant function or a parameterless process or action name, and the $s_i$ are sort names. In this rule, process $X(x_1, \ldots, x_n)$ is declared to have the same (potential) behaviour as the process expression $p$.

2.5 Basic processes

The two elementary operators to construct processes are the sequential composition operator, written as $p q$ and the alternative composition operator, written as $p + q$. The process $p q$ first performs the actions of $p$, until $p$ terminates, and then continues with the actions in $q$. It is common to omit the sequential composition operator in process expressions. The process $p + q$, behaves like $p$ or $q$, depending on which of the two performs the first action. Using the actions declared above, we can describe that action $a(3,d)$ must be performed, except if a time out occurs, in which case $a(t)$ must happen.

\[
a(3,d) + \text{timeout-a(t)}
\]

Note that the sequential operator binds stronger than the alternative composition operator.

The meaning of all process operators is given in terms of operational semantics in Appendix D. We will address a few issues about operational semantics in the next section, and skip further treatment here.
Table 2: Basic axioms for timed mCRL

<table>
<thead>
<tr>
<th>A1</th>
<th>(x + y = y + x)</th>
<th>SUM1</th>
<th>(\sum_{d \in D} x = x)</th>
</tr>
</thead>
<tbody>
<tr>
<td>A2</td>
<td>(x + (y + z) = (x + y) + z)</td>
<td>SUM3</td>
<td>(\sum X = \sum X + Xd)</td>
</tr>
<tr>
<td>A3</td>
<td>(x + x = x)</td>
<td>SUM4</td>
<td>(\sum_{d \in D} (Xd + Yd) = \sum X + \sum Y)</td>
</tr>
<tr>
<td>A4</td>
<td>((x + y) \cdot z = x \cdot z + y \cdot z)</td>
<td>SUM5</td>
<td>((\sum X) x = \sum_{a \in D} (Xd \cdot x))</td>
</tr>
<tr>
<td>A5</td>
<td>((x \cdot y) \cdot z = x \cdot (y \cdot z))</td>
<td>SUM11</td>
<td>((\forall d \in D Xd = Yd) \rightarrow \sum X = \sum Y)</td>
</tr>
<tr>
<td>AT6</td>
<td>(x + \delta 0 = x)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>A7</td>
<td>(\delta \cdot x = \delta)</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

| Bool1 | \(\neg (t = f)\) | C1 | \(x < t \triangleright y = x\) |
| Bool2 | \(\neg (b = t) \rightarrow b = f\) | C2 | \(x < f \triangleright y = y\) |

In Table 2 axioms A1-A5 are listed describing the elementary properties of the sequential and alternative composition operators. For instance, the axioms A1, A2 and A3 express that + is commutative, associative and idempotent. In these and other axioms we use variables \(x, y\) and \(z\) that can be instantiated by process terms. Note that for process terms we use the letters \(p, q\) and \(r\).

2.6 Deadlock and encapsulation

Timed \(\mu\)CRL contains a constant \(\delta\), expressing that from now on, no action can be performed. It models the situation where no action can be performed, for instance in case a number of computers are waiting for each other, and henceforth not performing any action anymore. Henceforth, this constant is called deadlock. A typical property for \(\delta\) is \(p + \delta = p\), provided \(p\) does not refer to time; the choice in \(p + q\) is determined by the first action performed by either \(p\) or \(q\), and therefore one can never choose for \(\delta\). However, if \(p\) refers to time, it can be that \(p\) must perform its first action before certain time. If that action does not occur before that time, \(p\) cannot be chosen anymore, and \(p + \delta\) acts like \(\delta\). In Table 2 the axiom AT6 is the variant of \(x + \delta = x\), where time is incorporated.

Another property of \(\delta\) is \(\delta \cdot p = \delta\). It says that whatever is after \(\delta\) cannot be reached. It is formulated as A7 in Table 2.

Sometimes, we want to express that certain actions cannot happen, and must be blocked. Generally, this is only done when we want to force this action into a communication. The encapsulation operator \(\partial_H\) is specially designed for this task. In \(\partial_H(p)\) it prevents all actions of which the action name is mentioned in \(H\) from happening. Typically,

\[
\partial_{\{b\}}(a \cdot b(3) \cdot c) = a \cdot \delta
\]

where \(a, b\) and \(c\) are actions. The properties of \(\partial_H\) are mentioned in Table 5.

2.7 Timed processes

A novel feature of timed \(\mu\)CRL is that it can be expressed at which time certain actions must take place. This is done using the \(at\) operator. The process \(p \cdot t\) (we use the letter \(t\) for terms of sort \(\text{Time}\)), behaves like the process \(p\), with the restriction that the first action of \(p\) must take place at time \(t\). So, assuming the natural numbers denote moments in time, the following process denotes that actions \(a\), \(b\) and \(c\) must take place at times 1, 2 and 3, respectively.

\(a \cdot 1 \cdot b \cdot 2 \cdot c \cdot 3\)
<table>
<thead>
<tr>
<th>Time1</th>
<th>( (t_1 \leq t_2 \lor t_2 \leq t_3 = t) \rightarrow t_1 \leq t_3 = t )</th>
</tr>
</thead>
<tbody>
<tr>
<td>Time2</td>
<td>( 0 \leq t = t )</td>
</tr>
<tr>
<td>Time3</td>
<td>( t_1 \leq t_2 \lor t_2 \leq t_1 = t )</td>
</tr>
<tr>
<td>Time4</td>
<td>( (t_1 \leq t_2 \land t_2 \leq t_1 = t) \leftrightarrow t_1 = t_2 )</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Time5</th>
<th>( \text{eq}(t_1, t_2) = t_1 \leq t_2 \land t_2 \leq t_1 )</th>
</tr>
</thead>
<tbody>
<tr>
<td>Time6</td>
<td>( \text{min}(t_1, t_2) = \text{if}(t_1 \leq t_2, t_1, t_2) )</td>
</tr>
<tr>
<td>Time7</td>
<td>( \text{if}(t, t_1, t_2) = t_1 )</td>
</tr>
<tr>
<td>Time8</td>
<td>( \text{if}(f, t_1, t_2) = t_2 )</td>
</tr>
</tbody>
</table>

\( \text{Bool} \)

| \( \neg t = f \) |
| \( \neg f = t \) |
| \( b \land t = b \) |
| \( b \lor f = f \) |
| \( b \lor t = t \) |
| \( b \lor f = b \) |

**Table 3:** Properties for **Time** and auxiliary functions for **Bool** and **Time**

\[
\begin{align*}
\text{ATA1} & \quad x = \sum_{t \in \text{Time}} x \cdot t \\
\text{ATA2} & \quad \delta \cdot t < t \leq t' \lor \delta \cdot 0 + c \cdot t' = c \cdot t' \\
\text{ATA3} & \quad c \cdot t \cdot x = c \cdot t \cdot (\sum_{t' \in \text{Time}} x \cdot t' < t \lor \delta \cdot 0 + \delta \cdot t') \\
\text{ATB1} & \quad \delta \cdot t' \cdot t' = \delta \cdot \text{min}(t, t') \\
\text{ATB2} & \quad c \cdot t \cdot t' = c \cdot t \cdot \text{eq}(t, t') \lor \delta \cdot \text{min}(t, t') \\
\text{ATB3} & \quad (x + y) \cdot t = x \cdot t + y \cdot t \\
\text{ATB4} & \quad (x \cdot y) \cdot t = x \cdot t \cdot y \\
\text{ATB5} & \quad (\sum_{d \in \mathcal{D}} X \cdot d) \cdot t = \sum_{d \in \mathcal{D}} X \cdot d \cdot t \\
\text{ATB6} & \quad (x \parallel y) \cdot t = x \cdot t \parallel y \\
\text{ATB7} & \quad (x \mid y) \cdot t = x \cdot t \mid y \\
\text{ATB8} & \quad (x \mid y) \cdot t = x \mid y \cdot t \\
\text{ATB9} & \quad \partial_H (x \cdot t) = \partial_H (x) \cdot t \\
\text{ATB10} & \quad \tau_H (x \cdot t) = \tau_H (x) \cdot t \\
\text{ATB11} & \quad \rho_H (x \cdot t) = \rho_H (x) \cdot t \\
\ll_1 & \quad x \ll c = x \\
\ll_2 & \quad x \ll (y + z) = x \ll y + x \ll z \\
\ll_3 & \quad x \ll y \cdot z = x \ll y \\
\ll_4 & \quad x \ll \sum_{d \in \mathcal{D}} X = \sum_{d \in \mathcal{D}} x \ll X \cdot d \\
\ll_5 & \quad x \ll y \cdot t = \sum_{t' \in \text{Time}} x \ll y \cdot t' < t \lor \delta \cdot 0 \\
\end{align*}
\]

**Table 4:** Time related axioms for timed \( \mu \text{CRL} \)
If an action happens at time \( t \), then a subsequent action can take place at a time \( t \) or higher. This means that in the first and second example below the \( b \) action can happen, and in the third example \( b \) is blocked.

\[
\begin{align*}
&\text{\( a \cdot 2 \cdot b \cdot 3 \)} \\
&\text{\( a \cdot 2 \cdot b \cdot 2 \)} \\
&\text{\( a \cdot 2 \cdot b \cdot 1 \)}
\end{align*}
\]

Actually, the last example above is equivalent to \( a \cdot 2 \cdot \delta \cdot 2 \), saying that after action \( a \) takes place, we have a time deadlock. In order to let \( b \) take place as prescribed, we have to reverse time. As this is clearly in conflict with reality, we choose to stop time at time \( 2 \) (\( \delta \cdot 2 \)). This is our general approach. Whenever, a specification prescribes timing behaviour that cannot be realised, it will exhibit time deadlocks. So, before implementing a timed \( \mu \)CRL specification, it is a good habit to prove it time deadlock free.

Every process starts at time \( 0 \).

In order to be able to provide an axiomatisation for the parallel operator, the bounded initialization operator \( \ll \) is introduced. The process \( p \ll q \) behaves like \( p \), where the first action of \( p \) must happen before last moment on which the first action of \( q \) can happen. If this cannot be the case, \( p \ll q \) is a deadlock at the latest time the first action of \( q \) could have happened. Although the operator differs slightly from its similarly named operator in [1], we have decided to give it the same name.

Both the bounded initialisation and the ‘at’ operator are axiomatized using the axioms in Table 4. For these axioms we have used three auxiliary functions, namely \( \text{min}(t, t') \), giving the earliest of \( t \) and \( t' \), \( \text{eq}(t, t') \) determining whether \( t \) and \( t' \) are the same moments in time, and \( \text{if}(b, t, t') \) yielding \( t \) if \( b \) equals true, and \( t' \) otherwise. These auxiliary functions are defined in Table 3.

### 2.8 Sums and conditionals

The process expression \( p \cdot b \cdot v \cdot q \) where \( p \) and \( q \) are processes, and \( b \) is a data term of sort \( \text{Bool} \), behaves like \( p \) if \( b \) is equal to \( t \) (true) and if \( b \) is equal to \( f \) (false), behaves like \( q \). This operator is called the conditional operator, and operates precisely as a \( \text{then_if_else} \) construct. Using the conditional operator data influences process behaviour. For instance a counter, that counts the number of \( a \) actions that occur, issuing a \( b \) action and resetting the internal counter after 10 \( a \)'s, can be described by \( \text{Counter}(0) \) where \( \text{Counter} \) is declared by (we omit declaring the datatypes):

\[
\text{proc } \text{Counter} \left(n; \mathbb{N}\right) = a \cdot \text{Counter}(n + 1) \ll n < 10 \ll b \cdot \text{Counter}(0)
\]

The conditional operator is characterised by axioms C1 and C2 in Table 2. There are no more axioms needed, because all properties about the conditionals appear to be provable using axioms Boo1 and Boo2.

The sum operator \( \sum_{d \in D} P(d) \) behaves like \( P(d_1) + P(d_2) + \cdots \); i.e., as the choice between \( P(d_i) \) for any data term \( d_i \) taken from \( D \). This is generally used to describe a process that is reading some input. E.g., in the following example we describe a single place buffer, repeatedly reading a value \( d \) using action name \( r \), and then delivering that value via action name \( s \).

\[
\text{proc } \text{Buffer} = \sum_{d \in D} r(d) \cdot s(d) \cdot \text{Buffer}
\]

In Tables 2, 4 and 5 axioms for the sum operator are listed. The sum operator is a difficult operator, because it acts as a binder just like the lambda in the lambda calculus (see e.g., [4]). This introduces a range of problems with substitutions. In order to avoid having to deal with these explicitly, we allow the use of explicit lambda operators and variables representing functions from data to processes.

In the Tables the variables \( x, y \) and \( z \) may be instantiated with processes and the capital variables \( X \) and \( Y \) can be instantiated with functions from some data sort to processes. The sum operator \( \sum \) expects a function from a datatype to a process, whereas \( \sum_{d \in D} \) expects a process. When we substitute
a process \( p \) for a variable \( x \) or a function \( \lambda d: D.p \) for a variable \( X \) in the scope of a sum operator, no variable in \( p \) may become bound. If this appears to happen, we must first rename the variable \( d \) into, say \( e \), provided \( e \) does not occur in \( p \). This renaming is called \( \alpha \)-conversion. We consider processes modulo \( \alpha \)-conversion, so the expressions \( \sum_{d: D} p(d) \) and \( \sum_{e: D} p(e) \) are equal. Consequently, we may only substitute the action \( a(d) \) for \( x \) in the left hand side of SUM1 in Table 2, after renaming \( d \) into \( e \). So, SUM1 is a concise way of saying that if \( d \) does not appear in \( p \), then we may omit the sum operator in \( \sum_{d: D} p \).

As another example, consider axiom SUM4. It says that we may distribute the sum operator over a plus, even if the sum binds a variable. This can be seen by substituting for \( X \) and \( Y \) \( \lambda d: D.a(d) \), and \( \lambda d: D.b(d) \), where no variable becomes bound. After \( \beta \)-reduction, the left hand side of SUM4 becomes \( \sum_{d: D}(a(d) + b(d)) \) and the right hand side is \( \sum_{d: D}a(d) + \sum_{d: D}b(d) \).

Although, the sum and conditional operator seem rather straightforward operators on first sight, they have considerably strengthened the applicability and power of process algebra.

2.9 Internal actions and hiding

Abstraction is an important means to analyse communicating systems. It means that certain actions are made invisible, such that the relationship between the remaining actions becomes more clear. A specification can be proven equal to an implementation, consisting of a number of parallel processes, after hiding all communications between these components.

The hidden action or internal action is denoted by \( \tau \). It represents an action that can take place in a system, but that cannot be observed directly. The internal action is meant for analysis purposes, and hardly ever used in specifications, as it is very uncommon to specify that something unobservable must happen. We consider the treatment of internal actions, including formulating axioms that allow to manipulate them, beyond our current scope.

In order to make actions hidden, the hiding operator \( \tau_I \) is introduced, where \( I \) is a set of action names. The process \( \tau_I(p) \) behaves as the process \( p \) except that all actions with action names in \( I \) are renamed to \( \tau \). This is clearly characterized by the axioms in Table 6 and axiom ATB10 in Table 4.

2.10 Parallel processes

The parallel operator can be used to put processes in parallel. The behaviour of \( p \parallel q \) is the arbitrary interleaving of actions of the processes \( p \) and \( q \), assuming for the moment that there is no communication between \( p \) and \( q \). For example the process \( a \parallel b \) behaves the same as \( a \cdot b + b \cdot a \).

The parallel operator allows to describe intricate processes. For instance a bag reading natural numbers using action name \( r \) and delivering those via action name \( s \) can be described by:

\[
\text{act} \quad r, s: \mathbb{N} \\
\text{proc} \quad Bag = \sum_{n: \mathbb{N}} r(n) \cdot (s(n) \parallel Bag)
\]

Note that the elementary property of bags, namely that at most as many numbers can be delivered as have been received in the past, is satisfied by this description.

It is possible to let processes \( p \) and \( q \) in \( p \parallel q \) communicate. This can be done to declare in a communication section that certain action names can synchronize. This is done as follows:

\[
\text{comm} \quad a | b = c
\]

This means that if actions \( a(d_1, \ldots, d_n) \) and \( b(d_1, \ldots, d_n) \) can happen in parallel, they may synchronize and this synchronization is denoted by \( c(d_1, \ldots, d_n) \). If two actions synchronize, their arguments must be the same. In a communication declaration it is required that action names \( a, b \) and \( c \) are declared with exactly the same data sorts. It is not necessary that these sorts are unique. It is for example perfectly right to declare the three actions both with a sort \( \mathbb{N} \) and with a pair of sorts \( D \times \text{Bool} \).
Table 5: Axioms for parallelism in timed μCRL

If a communication is declared as above, synchronisation is another possibility for parallel processes. For example the process $a \parallel b$ is now equivalent to $a \cdot b + b \cdot a + c$. Generally, this is not quite what is desired, as the intention generally is that $a$ and $b$ do not happen on their own. Therefore, the encapsulation operator can be used. The process $\partial_{\{a, b\}}(a \parallel b)$ is equal to $c$.

Data transfer between parallel components occurs very often. Here we describe as an example a simplified instance of data transfer. One process sends a natural number $n$ via action name $s$, and another process reads it, via action name $r$ and then announces it via action name $a$. Using encapsulation and hiding operators we enforce the processes to communicate, and make the communication internal. We omit declaring the datatypes.

\begin{align*}
\text{map } n \\
\text{act } r, s, c, a : \mathbb{N} \\
\text{comm r | s = c} \\
\text{proc } p = \tau_{\{c\}} \partial_{\{r, a\}}(s(n) \parallel \sum_{m : \mathbb{N}} r(m) \cdot a(m))
\end{align*}

The process $p$ behaves the same as $\tau \cdot a(n)$, as is to be expected.

Processes that are put in parallel can have time constraints on their actions that are used for communication. It may happen that these constraints are incompatible. For instance a process can be able to perform a read action $r$ before or at time 10, whereas the corresponding send action occurs at time 11. This is expressed by $\partial_{\{r, a\}}(\sum_{t : \text{time}} (r(t) \cdot a(t) \leq 10 \cdot a \cdot 0 \parallel s(11)))$. This process is equivalent to $\delta \cdot 10$, expressing that up till time 10, the process can execute without violating any timing constraint, but to reach any moment in time larger than 10, a timing constraint of an individual process must be ignored. In timed μCRL the timing constraints of individual processes are always respected.

Axioms that describe the parallel operator are in Tables 4 and 5. In order to formulate the axioms two auxiliary parallel operators have been defined. The left merge $\parallel$ is a binary operator that behaves exactly as the parallel operator, except that its first action must come from the left hand side. The communication merge $\mid$ is also a binary operator behaving as the parallel operator, except that the first action must be a synchronisation between its left and right operand. The core law for the parallel operator is CM1 in Table 5. It says that in $x \parallel y$ either $x$ performs the first step, represented by
the summand $x \parallel y$, or $y$ can do the first step, represented by $y \parallel x$, or the first step of $x \parallel y$ is a communication between $x$ and $y$, represented by $x \mid y$. All other axioms in Tables 4 and 5 are designed to eliminate the parallel operators in favour of the alternative and the sequential operator.

### 2.11 Renaming

In some cases it is efficient to reuse a given specification with different action names. This for instance allows the definition of generic components that can be used in different configurations. The renaming operator $\rho_R$ is suited for this purpose. The subscript $R$ is a sequence of renamings of the form $a \rightarrow b$, meaning that action name $a$ must be renamed to $b$. As $R$ is supposed to be a function every action name at the left hand side of an arrow must be unique in $R$. The process $\rho_R(p)$ behaves like the process $p$ with its action names renamed according to $R$. An equational characterisation of the renaming operator can be found in Tables 4, 5 and 6.

### 2.12 Notational conventions

As stated elsewhere, we generally do not denote the sequential composition operator.

Furthermore, instead of writing $\sum_{t: \text{Time}} p(t) \cdot c(t) \cdot \delta = 0$ indicating the process $p$ being executed at times $t$ for which $c(t)$ holds, we sometimes write the more readable $\sum_{t: c(t)} p(t)$. The $t$ at the left hand side of the colon indicates that it is the variable that is bound in the sum operator.

### 2.13 Fischer’s mutual exclusion protocol

As an example we describe Fischer’s mutual exclusion protocol [30, 34]. This protocol guarantees that only one single party can be in a critical section at any given time. The idea is that an application process can ask the protocol, using a request action, to organise exclusive access for some critical region. The protocol then executes a simple program, and will after some time respond with an enter action, indicating that the application process has now exclusive access. When the application process is ready, it issues a leave instruction, indicating to the protocol that it does not require exclusive access anymore.

We describe this protocol for only two parties, although it can be used for any arbitrary number. The main idea behind the protocol is that each party after being asked to access a critical region, checks whether the common variable $x$ equals 0, indicating that no process has claimed access. It then quickly sets the variable to its own sequence number to claim the critical section, and checks whether the variable has still the same value after a sufficiently long delay, guaranteeing that no other process will get access to the section. When leaving the critical region, the protocol sets the variable $x$ back to 0.

Before specifying the process behaviour we describe standard datatypes, where $eq$ on natural numbers represents the equality function, and $d$ is a positive time duration. As we assume that the sorts

| Table 6: Axioms for hiding and renaming in timed $\mu$CRL |
| --- | --- | --- | --- |
| T1D | $\tau_I(\delta) = \delta$ | RD | $\rho_R(\delta) = \delta$ |
| T1T | $\tau_I(\tau) = \tau$ | RT | $\rho_R(\tau) = \tau$ |
| T11 | $\tau_I(a(d)) = a(d)$ if $a \notin I$ | R1 | $\rho_R(a(d)) = R(a)(d)$ |
| T12 | $\tau_I(a(d)) = \tau$ if $a \in I$ | R3 | $\rho_R(x + y) = \rho_R(x) + \rho_R(y)$ |
| T13 | $\tau_I(x + y) = \tau_I(x) + \tau_I(y)$ | R4 | $\rho_R(x \cdot y) = \rho_R(x) \cdot \rho_R(y)$ |
and functions are self evident, we do not explain these further.

The processes asking for access use a common variable \( x \), which is modelled as a process \( X \). As can be seen, the variable \( x \) can be set to a new value, and can be tested for the value it contains.

There are two processes that control gaining access, namely \( FP(1) \) and \( FP(2) \). If \( FP(n) \) \( (n=1,2) \) is asked to arrange access to the critical region, it carries out a protocol described in \( FP_0(n) \). First it checks whether variable \( x \) is equal to 0, and records the time at which this action successfully takes place in variable \( t \). Then, within \( d \) time, it sets \( x \) to value \( n \), and subsequently after at least \( d \) time reads the value of \( x \). If this value is equal to \( n \), \( FP_0(n) \) terminates, allowing the enter action to happen. Otherwise, it starts the protocol described in \( FP_0(n) \) over again.

Note that it is assumed in this specification that the shared variable can be accessed at any time. This may not be realistic, and if so, it must explicitly be modelled that shared variables have certain response times and cannot be accessed too often in a certain time interval. We do not address this aspect here.

\[
\begin{align*}
\text{sort} & \quad \text{Time} \rightarrow \mathbb{N} \cup \text{Bool} \\
\text{func} & \quad t, f : \text{Time} \rightarrow \text{Bool} \\
& \quad 0 : \rightarrow \mathbb{N} \\
& \quad S : \mathbb{N} \rightarrow \text{Time} \\
\text{map} & \quad 0 : \rightarrow \text{Time} \\
& \quad \leq, \geq : \text{Time} \times \text{Time} \rightarrow \text{Bool} \\
& \quad + : \text{Time} \times \text{Time} \rightarrow \text{Time} \\
& \quad 1, 2 : \rightarrow \mathbb{N} \\
& \quad eq : \mathbb{N} \times \mathbb{N} \rightarrow \text{Bool} \\
& \quad d : \rightarrow \text{Time} \\
& \quad \neg : \text{Bool} \rightarrow \text{Bool} \\
\text{var} & \quad t_1, t_2, u : \text{Time} \\
& \quad x, y : \mathbb{N} \\
\text{rew} & \quad 1 = S(0) \\
& \quad 2 = S(1) \\
& \quad eq(0, 0) = t \\
& \quad eq(0, S(x)) = f \\
& \quad eq(S(x), 0) = f \\
& \quad eq(S(x), S(y)) = eq(x, y) \\
& \quad t_1 + u \leq t_2 + u = t_1 \leq t_2 \\
& \quad t_1 \leq t_1 + u = t \\
& \quad t_1 > t_2 = \neg(t_1 \leq t_2) \\
& \quad \neg f = t \\
& \quad \neg t = f \\
\text{act} & \quad \text{req, enter, leave, set}_x, \overline{\text{set}}_x, \text{test}_x, \overline{\text{test}}_x, \text{test}^* \rightarrow \mathbb{N} \\
\text{comm} & \quad \text{set}_x \rightarrow \overline{\text{set}}_x = \overline{\text{set}}^*_x \\
& \quad \overline{\text{test}}_x \rightarrow \text{test}_x = \overline{\text{test}}^*_x \\
\text{proc} & \quad X(x : \mathbb{N}) = \sum_{y \in \mathbb{N}} \text{set}_x(y) \ X(y) + \overline{\text{test}}_x(x) \ X(x) \\
& \quad FP(n : \mathbb{N}) = \text{req}(n) \ FP_0(n) \ \text{enter}(n) \ \text{leave}(n) \ \text{set}_x(0) \ FP(n) \\
& \quad FP_0(n : \mathbb{N}) = \sum_{t : \text{Time}} \text{test}_x(0) \ t \ \sum_{t' : \text{Time}} \text{set}_x(n) \ t' \sum_{t'' : \text{Time}} \text{test}_x(n) \ t'' \text{eq}(m, n) \ \text{set}_x(m) \ t'' \ FP_0(n) \\
\text{init} & \quad \tau_{\{\text{set}_x, \text{test}_x\}} \partial_{\{\text{set}_x, \overline{\text{set}}_x, \text{test}_x, \overline{\text{test}}_x\}} \ (FP(1) \ || \ X(0) \ || \ FP(2))
\end{align*}
\]
3 Algebras, operational semantics and strong bisimulation

In order to establish a common intuition about the meaning of each data term a model in the form of a data algebra is provided and to establish an intuition for process terms, a transition system is associated to it.

Data algebras are defined in section C. The basic idea is that for every declared sort there is a set of elements in the data algebra. Every data term is interpreted as such an element. A data algebra is called a model if all equations of a datatype hold in the algebra.

For the semantics of a process term, we use a timed transition system, which is defined in Definitions D.2 and D.3. A timed transition system essentially tells under which actions one can traverse from one state to another. States are pairs of a process term and a moment in time. There are two sorts of transitions, namely action transitions and timing transitions. Action transitions are labelled by an action name, followed by zero or more elements from an algebraic domain. We follow the urgent model of [2, 27] and choose that the occurrence of an action does not take time. I.e. if

\[ \langle p, t \rangle \xrightarrow{a(t)} \langle p', t' \rangle, \]

then \( t = t' \). The rules in Tables 9, 10 and 11 define when a transition can do a step. These rules are provided in the SOS style [32, 20].

Time progresses through time transitions which are labelled with an idle step \( \iota \), e.g.

\[ \langle p, t \rangle \xrightarrow{\iota} \langle p, t' \rangle \]

expresses that process \( p \) idles from \( t \) to \( t' \). The rule in Table 8 together with Definition D.1 of the ultimate delay define when idle steps can be performed.

Given the transition relation we have a good understanding of the steps that can be performed by a process. An obvious question is when two processes are actually equivalent. Such a notion is required to justify the axioms. There is no clear answer given in the literature (see [12, 13] for an overview), but there is a general consensus that strong bisimulation is the finest equivalence of all potential candidates, and henceforth, that two processes are certainly equivalent if they are strongly bisimilar. Therefore, we provide below timed strong bisimulation.

**Definition 3.1.** Let \( E \) be a specification and let \( \mathcal{A} \) be a model for the datatypes in \( E \). Let \( P \) be the set of all process terms, and let \( \sqrt{} \) be a special symbol indicating termination. Moreover, let \( \mathcal{D}_{\text{Time}} \) be the domain in which elements of sort \( \text{Time} \) are interpreted (see Definition C.6). A collection of symmetric relations \( \langle R_t^{\mathcal{A}} \subseteq (P \cup \{\sqrt{}\}) \times (P \cup \{\sqrt{}\}) \mid t \in \mathcal{D}_{\text{Time}} \rangle \) is called a timed (strong) bisimulation iff for all \( p, q \in P \cup \{\sqrt{}\} \) and \( t \in \mathcal{D}_{\text{Time}} \) such that \( pR_t^{\mathcal{A}}q \), it holds that:

1. if \( p \equiv \sqrt{} \), then \( q \equiv \sqrt{} \),

2. if \( \langle p, t \rangle \xrightarrow{\iota} \langle p', t \rangle \), then there is a \( q' \) such that \( \langle q, t \rangle \xrightarrow{\iota} \langle q', t \rangle \), and \( pR_t^{\mathcal{A}}q' \).

3. if \( \langle p, t \rangle \xrightarrow{a(t)} \langle p', t' \rangle \) then \( \langle q, t \rangle \xrightarrow{a(t)} \langle q', t' \rangle \) and \( pR_t^{\mathcal{A}}q \).

We write \( p \equiv^t q \) iff for model \( \mathcal{A} \) there is a timed (strong) bisimulation \( \langle R_t^{\mathcal{A}} \mid t \in \mathcal{D}_{\text{Time}} \rangle \) such that \( pR_tq \), and we write \( p \equiv q \) if for all \( t \in \mathcal{D}_{\text{Time}} \) \( p \equiv^t q \). In this case we say that \( p \) and \( q \) are timed (strongly) bisimilar.

Timed bisimulation is a congruence for the process operators introduced in this text and all axioms are sound with respect to it. There has not been any attempt to provide a complete set of axioms. For a treatment of completeness issues, as well as weak timed bisimulations, the reader is for instance referred to [26, 27, 10].
4 Examples of simple verifications

An important usage of the axioms of timed $\mu$CRL is to verify the correctness of distributed systems. Manipulating terms using the axioms is not always easy, but generally boils down to a typical number of ever occurring steps. In order to allow a deeper understanding of the language and its axioms we provide a number of typical verification examples. We restrict ourselves to essentially non recursive processes and do not employ the hidden nature of $\tau$. For these we need rules such as the recursive specification principle (RSP) and tau laws for timed process algebra, which we have not provided.

4.1 Data

4.1.1 Proving terms unequal

In $\mu$CRL it is possible to establish when two data terms are not equal. This is for instance required in order to establish that two processes cannot communicate. There is a characteristic way of proving that terms are not equal, namely by assuming that they are equal, and showing that this implies $t = f$, contradicting axiom $\text{Bool}1$.

We give an example showing that the natural numbers zero (0) and one ($S(0)$) are not equal. We assume that the natural numbers with a 0 and successor function $S$ are appropriately declared. In order to show zero and one different, we need a function that relates $N$ to $\text{Bool}$. Note that if there is no such function, there are models of the datatype $N$ where zero and one are equal, and then we can of course not prove them different. For our function we choose ‘less or equal than’ on the natural numbers, defined as follows:

\begin{align*}
\text{map} & \leq : N \times N \to N \\
\text{var} & n, m : N \\
\text{rew} & 0 \leq n = t \\
& S(n) \leq 0 = f \\
& S(n) \leq S(m) = n \leq m
\end{align*}

Now assume $0 = S(0)$. Clearly, $0 \leq 0 = t$. But, using the assumption, we also find $0 \leq 0 = S(0) \leq 0 = f$. So, we can prove $t = f$. Hence, we may conclude $0 \neq S(0)$.

4.1.2 Using induction on $\text{Bool}$

An easy and very convenient axiom is $\text{Bool}2$. It says that if a boolean term $b$ is not equal to true, it must be equal to $f$ or in other words that there are at most two boolean values. Applying this axiom boils down to a case distinction, proving a statement for the values true and false, and concluding that the property must then universally hold. We refer to this style of proof by the phrase ‘induction on booleans’.

A typical example is the proof of $b \land b = b$. Using induction on $\text{Bool}$, it suffices to prove that this equality hold for $b = t$ and $b = f$. In other words, we must show that $t \land t = t$ and $f \land f = f$. These are trivial instances of axioms $\text{Bool}5$ and $\text{Bool}6$ in Table 3.

Note that among many others, the equalities $b \lor b = b$ and $\neg \neg b = b$ can be shown in the same way.

4.1.3 Identities about time

We list a number of identities about time which we use later on. Although the identities appear very straightforward, their proofs sometimes are slightly tricky. In these cases some twist is needed to derive these identities from the axioms. We provide proofs of the first two equations.

Lemma 4.1.

1. $eq(t_0, t_1) = t$ iff $t_0 = t_1$;
2. \( t_1 \leq t_0 = t \rightarrow \min(t_0, t_1) = t_1 \);
3. \( \min(t_0, t_1) = \min(t_1, t_0) \);
4. \( t_0 \leq t_1 \land t_0 \leq t_2 = t_0 \leq \min(t_1, t_2) \);
5. \( t_0 \leq \min(t_0, t_1) = t_0 \leq t_1 \).

**Proof.**

1. Using axioms Time3 and Time4 from Table 3 this is trivial.
2. We prove this fact by a case distinction on \( t_0 \leq t_1 \).
   \[
   \begin{align*}
   &\bullet t_0 \leq t_1 = f \text{ It follows that } \min(t_0, t_1) \overset{\text{Time5}}{=} \text{if}(t_0 \leq t_1, t_0, t_1)^\text{assumption} \overset{\text{if}(f, t_0, t_1) \overset{\text{Time6}}{=} t_1.}{}
   \\
   &\bullet t_0 \leq t_1 = t \text{ This side is somewhat more involved. First we show that } t_0 \text{ and } t_1 \text{ are equal:}
   \end{align*}
   \]
   \[
   \begin{align*}
   &eq(t_0, t_1) \overset{\text{Time4}}{=} t_0 \leq t_1 \land t_1 \leq t_0^\text{assumption} \overset{\text{t} \land \text{t} \overset{\text{Bool5}}{=} \text{t}}{=}
   \\
   &\text{So, using Lemma 4.1, it follows that } t_0 = t_1. \text{ Now the proof is trivial:}
   \end{align*}
   \]
   \[
   \begin{align*}
   &\min(t_0, t_1) \overset{\text{Time6}}{=} \text{if}(t_0 \leq t_1, t_0, t_1)^\text{assumption} \overset{\text{if}(t, t_0, t_1) \overset{\text{Time7}}{=} t_0 = t_1.}
   \end{align*}
   \]

\[\square\]

4.1.4 Induction on \( \mathbb{N} \)

The division between constructors and mappings gives us induction principles. Suppose we have declared the natural numbers with constructors zero and successor. If we extend this with a mapping `add` as follows:

\[
\text{map} \quad \text{add}: \mathbb{N} \times \mathbb{N} \rightarrow \mathbb{N}
\]

\[
\text{var} \quad n, m : \mathbb{N}
\]

\[
\text{rew} \quad \text{add}(n, 0) = n
\]

\[
\text{add}(n, S(m)) = S(\text{add}(n, m))
\]

we can for instance in a fully standard fashion derive that \( \text{add}(0, n) = n. \) We apply induction on the constructors for \( \mathbb{N}. \)

First, we must show that \( \text{add}(0, 0) = 0. \) This is a trivial instance of the first axiom about add.

Secondly, assuming \( \text{add}(0, n) = n, \) we must show \( \text{add}(0, S(n)) = S(n). \) This follows by:

\[
\text{add}(0, S(n)) = S(\text{add}(0, n)) = S(n).
\]

In a similar way, induction can be used on any datatype declared with constructors.

4.2 Processes

4.2.1 Conditionals

We list some simple identities between processes containing conditionals. As the proofs of these identities straightforward, only the first one is proven. Note that there are many more of these identities; we have not at all attempted to give an exhaustive list.

**Lemma 4.2.**

1. \( x \triangleleft b \triangleright y = x \triangleleft b \triangleright 0 + y \triangleleft \neg b \triangleright 0; \)
2. \( x \triangleleft b_1 \lor b_2 \Rightarrow \delta \cdot 0 = x \triangleleft b_1 \Rightarrow \delta \cdot 0 + x \triangleleft b_2 \Rightarrow \delta \cdot 0; \)

3. \( (b = t \Rightarrow x = y) \Rightarrow x \triangleleft b \Rightarrow z = y \triangleleft b \Rightarrow z; \)

4. \( x \triangleleft b \Rightarrow y \parallel z = x \parallel z \triangleleft b \Rightarrow y \parallel z. \)

**Proof.** All these identities are proven with induction on booleans. The first identity for \( b = t \) states \( x = x + \delta \cdot 0 \) which is exactly axiom AT6. For \( b = f \) it says \( y = \delta \cdot 0 + y \), which using axiom A1 and AT6 is also trivial to prove. \( \square \)

### 4.2.2 Summand inclusion

For processes we use the shorthand \( x \subseteq y \) for \( x + y = y \) and we write \( x \supseteq y \) for \( y \subseteq x \). This notation is called *summand inclusion*. It is useful to divide the proof of an equality into proving two inclusions, as the following lemma shows.

**Lemma 4.3** *(Summand inclusion).*

\[ x \subseteq y, \ y \subseteq x \Rightarrow x = y \]

**Proof.** By assumption we know that \( x + y = y \) and \( y + x = x \). Hence, \( x = y + x \stackrel{A1}{=} x + y = y \). \( \square \)

### 4.2.3 Expansion of the parallel operator

Primary to many verifications is the *expansion* of the parallel operator. This means that the parallel operator is removed in favour of the alternative and sequential operator. Given that we want to have a finite set of axioms which require the auxiliary left and communication merge operator, and the bounded initialisation operator. Below we give a typical example of such an expansion. Note that due to the presence of time these expansions differ slightly from those in [3].

Assume a sort \( D \) is declared with at least an element \( d_0 \), together with three actions \( c, r, s \) of sort \( D \) such that \( r \) and \( s \) communicate to \( c \) (i.e. \( \gamma(r, s) = c \)).

\[
\begin{align*}
\sum_{d:D} r(d) \parallel s(d_0) &\equiv^{CM1} 1 \\
\sum_{d:D} r(d) \parallel s(d_0) + s(d_0) \parallel \sum_{d:D} r(d) + \sum_{d:D} r(d) \parallel s(d_0) &\equiv^{SUM6,SUM7} 4 \\
\sum_{d:D} r(d) \parallel s(d_0) + s(d_0) \parallel \sum_{d:D} r(d) + \sum_{d:D} r(d) \parallel s(d_0) &\equiv^{CM2,CF} 2 \\
\sum_{d:D} r(d) \parallel s(d_0) \parallel s(d_0) \parallel \sum_{d:D} r(d) \parallel s(d_0) \parallel \sum_{d:D} r(d) \parallel s(d_0) \parallel \sum_{d:D} r(d) \parallel s(d_0) \parallel \sum_{d:D} c(d) \parallel eq(d, d_0) \parallel \delta &\equiv^{S4} 3 \\
\sum_{d:D} r(d) \parallel s(d_0) \parallel s(d_0) \parallel \sum_{d:D} r(d) \parallel s(d_0) \parallel \sum_{d:D} r(d) \parallel s(d_0) \parallel \sum_{d:D} c(d) \parallel eq(d, d_0) \parallel \delta &\equiv^{S4} 3 \\
\end{align*}
\]

Below it is illustrated how the result of the communication can be further simplified, and how communication between \( r \) and \( s \) is enforced.

### 4.2.4 Elimination of a finite sum

We show how the following identity can be proven:

\[
\sum_{n \in \mathbb{N}} r(n) \triangleleft n \leq 2 \Rightarrow \delta = r(0) + r(1) + r(2)
\]

assuming that the natural numbers together with the \( \leq \) relation have appropriately been defined. The result follows in a straightforward way using the following lemma that we prove first.

**Lemma 4.4.** For all \( m \in \mathbb{N} \) we find \( (S \infty \text{ the successor function}) \):

\[
\sum_{n \in \mathbb{N}} Xn = X0 + \sum_{m \in \mathbb{N}} XS(m).
\]
Proof. Using Lemma 4.3 we can split the proof into two summand inclusions.

\( \subseteq \) We first prove the following statement with induction on \( n \):

\[
Xn \subseteq X0 + \sum_{m: N} XS(m). \tag{2}
\]

- \( n = 0 \) Trivial using A3.
- \( n = S(n') \)

\[
X0 + \sum_{m: N} XS(m) \overset{\text{SUM3}}{=} X(0) + \sum_{m: N} XS(m) + XS(n') \subseteq Xn.
\]

So (2) has been proven without assumption on \( n \) (i.e. for all \( n \)). Hence, application of SUM11, SUM4 and SUM1 yields:

\[
\sum_{m: N} Xn \subseteq X0 + \sum_{m: N} XS(m),
\]

as had to be shown.

\( \supseteq \) Using SUM3 it immediately follows that for all \( m \)

\[
\sum_{m: N} Xn \supseteq X0 + XS(m).
\]

So, SUM11, SUM4 and SUM1 yield:

\[
\sum_{m: N} Xn \supseteq X0 + \sum_{m: N} XS(m).
\]

\( \square \)

Equation (1) can now easily be proven by:

\[
\sum_{m: N} r(n) \triangleleft n \leq 2 \triangleright \delta \overset{\text{Lemma 4.4}}{=} r(0) \triangleleft 0 \leq 2 \triangleright \delta + \sum_{m: N} r(S(n')) \triangleleft S(n') \leq 2 \triangleright \delta \overset{\text{Lemma 4.4}}{=} r(0) + r(S(0)) \triangleleft S(0) \leq 2 \triangleright \delta + \sum_{m: N} r(S(n'')) \triangleleft S(n'') \leq 2 \triangleright \delta = r(0) + r(1) + r(2) + \delta
\]

Note that we use that we can prove that \( 0 \leq 2 = t, S(0) \leq 2 = t, S(S(0)) \leq 2 = t \text{ and } S(S(S(0))) \leq 2 = f \). According to Lemma 4.6.1 below we may omit the last \( \delta \).

4.2.5 Abuse of SUM11

One of the most tricky rules of \( \mu \)CRL is SUM11. The universal quantifier expresses that the rule may only be applied when there are no assumptions made on \( d \). Here we show what can go wrong.

First, assume that using the elimination of a finite sum, we have proven \( \sum_{b: B} x \triangleleft b \triangleright y = x + y \). This fact is valid, although the proof is slightly tricky. An easy corollary of this fact is that \( \sum_{b: B} x \triangleleft b \triangleright y = \sum_{b: B} y \triangleleft b \triangleright x \). We prove below the invalid equation \( x = \sum_{b: B} x \triangleleft b \triangleright y \). Note that this equation easily allows us to conclude that \( x = \sum_{b: B} x \triangleleft b \triangleright y = \sum_{b: B} y \triangleleft b \triangleright x = y \), collapsing the whole domain of processes.
The erroneous proof of $\sum_{b: \text{Bool}} x \triangleleft b \triangleright y = x$ goes with induction on $b$. First take $b = \text{t}$. Clearly, $x \triangleleft b \triangleright y = x$, using axiom Cl. Hence, using SUM11 and SUM1, $\sum_{b: \text{Bool}} x \triangleleft b \triangleright y = x$. Second, consider the case where $b = \text{f}$. Using C2 we find $y \triangleleft b \triangleright x = x$. Hence, using SUM11 and SUM1 we find $\sum_{b: \text{Bool}} y \triangleleft b \triangleright x = x$. According to the observation above, we may exchange the position of $x$ and $y$ in the left hand side. So, we find the desired $\sum_{b: \text{Bool}} x \triangleleft b \triangleright y = x$. As we have proven the equation for $b = \text{t}$ and $b = \text{f}$, we may conclude that it universally holds.

The problem of course in this example is that when applying SUM11, the premise is only valid for certain $b$, instead of all $b$, namely $b = \text{t}$ and $b = \text{f}$ respectively.

Despite this pitfall, we quite often use sequences $\sum \ldots = \sum \ldots = \sum \ldots$, where we transform the process terms at \ldots using, axioms and lemmas. See for instance the proof of Lemma 4.6.2. In such cases we carefully check that the transformation is valid for every variable bound by the sum operator, and we silently apply SUM11.

4.2.6 Sum elimination

An important law is sum elimination. It states that the sum over a datatype from which only one element can be selected can be removed. This lemma occurred for the first time in [14]. Note that we assume that we have a function $\text{eq}$ available, reflecting equality between terms. We provide two variants. A general one, for use in timed setting, and a specific one for specifications in which time does not play a role. We only provide the proof of the first one.

**Lemma 4.5** (Sum elimination). Let $D$ be a sort and $\text{eq}: D \times D \rightarrow \text{Bool}$ a function such that for all $d, e: D$ it holds that $\text{eq}(d, e) = \text{t}$ iff $d = e$. Then

- $\sum_{d: D} Xd \triangleleft \text{eq}(d, e) \triangleright \delta \cdot 0 = Xe$.
- $\sum_{d: D} c(d) \cdot Xd \triangleleft \text{eq}(d, e) \triangleright \delta = c(e) \cdot Xe$.

**Proof.** According to Lemma 4.3 it suffices to prove summand inclusion in both directions.

$\subseteq$) Using Lemma 4.2.1 above we find:

$$\forall d: D. Xe = Xe \triangleleft \text{eq}(d, e) \triangleright \delta \cdot 0 + Xe \triangleleft \neg \text{eq}(d, e) \triangleright \delta \cdot 0.$$ 

We may put the $\forall d: D$ in front of the formula, because we did not make any assumption about $d$. Using SUM11, SUM4, Lemma 4.2.3 and the assumption that $\text{eq}(d, e) = \text{t} \rightarrow d = e$, we find:

$$\sum_{d: D} Xe = \sum_{d: D} Xd \triangleleft \text{eq}(d, e) \triangleright \delta \cdot 0 + \sum_{d: D} Xe \triangleleft \neg \text{eq}(d, e) \triangleright \delta \cdot 0.$$ 

Using SUM1 and the summand inclusion notation we obtain:

$$\sum_{d: D} Xd \triangleleft \text{eq}(d, e) \triangleright \delta \cdot 0 \subseteq Xe.$$ 

$\supseteq$) By applying SUM3, and the assumption that $\text{eq}(e, e) = \text{t}$, we find:

$$\sum_{d: D} Xd \triangleleft \text{eq}(d, e) \triangleright \delta \cdot 0 \supseteq Xe \triangleleft \text{eq}(e, e) \triangleright \delta \cdot 0 = Xe.$$ 

$\square$
4.2.7 Data transfer

We show how we can verify that data sent by one process indeed arrives at the recipient, who can then use it further. Assume we are given a data domain \( D \) with at least an element \( d_0 \) and an equality function \( eq \). We define the following sender process \( S \) (the variable \( x \) denotes some arbitrary continuation of the process):

\[ \text{proc } S = s(d_0) \cdot x \]

A receiving process can be defined as follows (\( Yd \) is a continuing process in which the received value \( d \) is used):

\[ \text{proc } R = \sum_{d \in D} r(d) \cdot Yd \]

We let the send \( s \) and receive \( r \) actions synchronize by declaring:

\[ \text{comm } s \parallel r = c \]

And we should be able to show that:

\[ \partial_{\{r, s\}}(S \parallel R) = c(d_0) \cdot \partial_{\{r, s\}}(x \parallel Yd_0) \]

where the encapsulation operator \( \partial_{\{r, s\}} \) enforces the \( r \) and \( s \) action to synchronize.

The verification uses the following steps, where the expansion step is similar to the one given in section 4.2.3:

\[ \begin{align*}
\partial_{\{r, s\}}(S \parallel R) & \overset{\text{Expansion}}{=} \partial_{\{r, s\}}(s(d_0) \cdot (x \parallel R)) + \sum_{d \in D} r(d) \cdot (S \parallel Yd) + \sum_{d \in D} c(d) \cdot (x \parallel Yd) \triangleq eq(d, d_0) \triangleright \delta \\
& \overset{\text{Sum elimination}}{=} \sum_{d \in D} c(d) \cdot \partial_{\{r, s\}}((x \parallel Yd) \triangleq eq(d, d_0) \triangleright \delta)
\end{align*} \]

4.2.8 Process identities involving time

The first identity below is an instance of the law \( x + \delta = x \) in process algebra that is invalid in timed \( \mu \text{CRL} \). It illustrates however that \( x + \delta = x \) is still valid, if \( x \) does not refer to time. The second identity is used in the next example.

**Lemma 4.6.**

1. \( c + \delta = c \)
2. \( \sum_{t \in \text{Time}} \delta^{\min(t, t_0)} \cdot t \leq t_1 \triangleright \delta \cdot 0 = \delta^{\min(t_0, t_1)} \).

**Proof.**

1. \( c + \delta \overset{\text{ATA1} \text{ATA3}}{=} \sum_{t \in \text{Time}} (c \cdot t + \delta \cdot t) \overset{\text{ATA2}}{=} \sum_{t \in \text{Time}} c \cdot t \overset{\text{ATA1}}{=} c. \)
2. The proof of the second item is somewhat more involved:

\[ \begin{align*}
\sum_{t \in \text{Time}} \delta^{\min(t, t_0)} \cdot t \leq t_1 \triangleright \delta \cdot 0 & \overset{\text{Time6}}, 4.2.2}{=} \\
\sum_{t \in \text{Time}} \delta^{\min(t, t_0)} \cdot t \leq t_1 \bigwedge t \leq t_0 \triangleright \delta \cdot 0 & \overset{4.1.2, 4.2.3, \text{Time6}}{=} \\
\sum_{t \in \text{Time}} \delta^{t_0} \cdot t \leq t_0 \bigwedge t_0 \leq t \triangleright \delta \cdot 0 & \overset{4.1.2, 4.2.3}{=} \\
\delta^{\min(t_0, t_1)} + \delta^{t_0} \cdot t_0 \leq t_1 \triangleright \delta \cdot 0 \overset{4.1.2, 4.2.3}{=} \\
\delta^{\min(t_0, t_1)} + \delta^{\min(t_0, t_1)} \cdot t_0 \leq t_1 \triangleright \delta \cdot 0 \overset{4.1.2, 4.2.3}{=} \\
\delta^{\min(t_0, t_1)} \end{align*} \]
At *, we use the identities \( \sum_{t \in \text{Time}} \delta t_0 \preceq t \leq t_1 \wedge t_0 \preceq t \succ \delta \cdot 0 = \delta t_0 \preceq t_0 \leq t_1 \succ \delta \cdot 0 \) and \( \sum_{t \in \text{Time}} \delta t_3 \preceq t' \succ \delta \cdot 0 = \delta \cdot 0 \) which are straightforwardly provable using among others summand inclusion, ATA2, SUM3, SUM4 and SUM11.

\[ \square \]

### 4.2.9 Processes with time constraints

In this section we show what happens if processes with time constraints must communicate. In order to show what can happen we describe a process that sends via action name \( s \) at time \( t_0 \) to a process that receives via action name \( r \) before time \( t_1 \). We prove the combination of these processes equal to \( c t_0 \preceq t_0 \leq t_1 \succ \delta \cdot t_1 \). This says that if \( t_0 \) comes before \( t_1 \) the processes communicate at time \( t_0 \). If however \( t_0 \) comes after \( t_1 \), then if time would pass \( t_1 \), the constraints of the receiving party would be violated. Therefore, we find a deadlock at time \( t_1 \).

We describe the communicating processes as follows:

\[
\partial_{[r,s]}(s t_0 \parallel \sum_{t \in \text{Time}} r t \preceq t \leq t_1 \succ \delta \cdot 0).
\]

By applying the law CMI we reduce this term to

\[
\begin{align*}
\partial_{[r,s]}(s t_0 \parallel & \sum_{t \in \text{Time}} r t \preceq t \leq t_1 \succ \delta \cdot 0) + \\
\partial_{[r,s]}(s t_0 \parallel & \sum_{t \in \text{Time}} r t \preceq t \leq t_1 \succ \delta \cdot 0) + \\
\partial_{[r,s]}(s t_0 \parallel & \sum_{t \in \text{Time}} r t \preceq t \leq t_1 \succ \delta \cdot 0) \quad (3)
\end{align*}
\]

Now we prove the first and last summand of (3) in turn. The proof of the second summand goes in the same way as the proof of the first one and is therefore not given. The first summand of (3) can be shown equal to \( \delta \cdot \min(t_0, t_1) \) as follows:

\[
\begin{align*}
\partial_{[r,s]}(s t_0 \parallel \sum_{t \in \text{Time}} r t \preceq t \leq t_1 \succ \delta \cdot 0) & \overset{\text{SUM6, SUM8, 4.2.4}}{=} \\
\sum_{t \in \text{Time}} (\partial_{[r,s]}(r t \parallel s t_0) \preceq t \leq t_1 \succ \delta \cdot 0) & \overset{\text{CMI}}{=} \\
\sum_{t \in \text{Time}} (\partial_{[r,s]}(r t \parallel s t_0) \preceq t \leq t_1 \succ \delta \cdot 0) & \overset{\text{D1}, \text{D4}, \text{L5}}{=} \\
\sum_{t \in \text{Time}} (\partial_{[r,s]}(r t \parallel s t_0) \preceq t \leq t_1 \succ \delta \cdot 0) & \overset{\text{L1}, \text{TB1}}{=} \\
\sum_{t \in \text{Time}} (\partial_{[r,s]}(r t \parallel s t_0) \preceq t \leq t_1 \succ \delta \cdot 0) & \overset{\text{L2}}{=} \\
\sum_{t \in \text{Time}} (\partial_{[r,s]}(r t \parallel s t_0) \preceq t \leq t_1 \succ \delta \cdot 0) & \overset{\text{L2}}{=} \\
\partial_{[r,s]}(s t_0 \parallel \sum_{t \in \text{Time}} r t \preceq t \leq t_1 \succ \delta \cdot 0) & \overset{\text{L2}}{=} \\
\sum_{t \in \text{Time}} (\partial_{[r,s]}(r t \parallel s t_0) \preceq t \leq t_1 \succ \delta \cdot 0) & \overset{\text{L2}}{=} \\
\delta \cdot \min(t_0, t_1) & \overset{\text{L2}}{=}
\end{align*}
\]

Similarly, it can be shown that the second summand of (3) equals \( \delta \cdot \min(t_0, t_1) \).
We now prove the third summand of (3) equal to \(c \cdot t_0 < t_0 \leq t_1 \land \delta \cdot t_1\).

\[
\partial_{[r,s]}(s; t_0^r, t_1^r, t \leq t_1 \land \delta \cdot t_1) \quad \text{SUM}^r_{\sum s}\]
\[
\sum_{t_0^r; \text{Time}} \partial_{[r,s]}(s; t_0^r, t \leq t_1 \land \delta \cdot t_1) \quad \text{SUM}^r_{\sum s}\]
\[
\sum_{t_0^r; \text{Time}} (c \cdot t_0 < eq(t_0, t) \land t \leq t_1 \land \delta \cdot t_1) \quad \text{SUM}^r_{\sum s}\]
\[
\sum_{t_0^r; \text{Time}} c \cdot t_0 < eq(t_0, t) \land t \leq t_1 \land \delta \cdot t_1 \quad \text{SUM}^r_{\sum s}\]

Using the results above we can prove (3) equal to

\[
\delta \cdot min(t_0, t_1) + \delta \cdot min(t_0, t_1) + c \cdot t_0 < t_0 \leq t_1 \land \delta \cdot t_1\]
\[
(c \cdot t_0 + \delta \cdot min(t_0, t_1)) \land t \leq t_1 \land \delta \cdot t_1 \quad \text{SUM}^r_{\sum s}\]
\[
(c \cdot t_0 < t_0 \leq t_1 \land \delta \cdot t_1)\]

A The formal syntax of timed \(\mu\)CRL

We define a syntax of timed \(\mu\)CRL specifications. Contrary to the syntax used in the main text, this syntax is completely fixed; the syntax is meant for computer processing and intended for specifications of real systems, where ambiguities, including ambiguities in the syntax cannot be tolerated. The syntax uses standard (roman) symbols only, extended with some common punctuation symbols.

The syntax is defined in the Syntax Definition Formalism (SDF) [22]. According to the convention in SDF we write syntactical categories with a capital, and keywords with small letters. The first LAYOUT rule says that spaces (\'\ '), tabs (\'\t\') and newlines (\'\n\') may be used to generate some attractive layout and are not part of the \(\mu\)CRL specification itself. The second LAYOUT rule says that lines starting with a \%' sign followed by zero or more non-newline characters (\'\%\[\n\]\') followed by a newline (\'\n\') must be taken as comments and are therefore also not a part of the \(\mu\)CRL syntax.

A Name is an arbitrary string over a-z, A-Z, 0-9 and the special characters \'\%\[\n\]\' followed by a by newline (\'\n\') must be taken as comments and are therefore also not a part of the \(\mu\)CRL syntax.

A Name is an arbitrary string over a-z, A-Z, 0-9 and the special characters \'\%\[\n\]\' followed by a newline (\'\n\') must be taken as comments and are therefore also not a part of the \(\mu\)CRL syntax.

A Name is an arbitrary string over a-z, A-Z, 0-9 and the special characters \'\%\[\n\]\' followed by a newline (\'\n\') must be taken as comments and are therefore also not a part of the \(\mu\)CRL syntax.

The phrase \{right\} means that an operator is right-associative and \{assoc\} means that an operator is associative. The phrase \{bracket\} says that the defined construct is not an operator, but just a way to disambiguate the construction of a syntax tree.

The priorities say that the operator \'\&\' has highest and + has lowest priority when parsing process terms with ambiguous bracketing.
exports
sorts Name
  Name-list
  Domain
  Sort-specification
  Function-specification
  Function-declaration
  Equation-specification
  Variable-declaration
  Variables
  Data-term
  Equation-section
  Single-equation
  Process-term
  Renaming
  Variable
  Process-specification
  Process-declaration
  Action-specification
  Action-declaration
  Communication-specification
  Communication-declaration
  Initial-declaration
  Specification

lexical syntax
[ \n\n] → LAYOUT
"%" "[\n]* "\n" → LAYOUT
[a-zA-Z0-9-_`-]+ → Name
context-free syntax
{ Name "," }+ → Name-list
{ Name "," }+ → Domain
"sort" Name+ → Sort-specification
"func" Function-declaration+ → Function-specification
"map" Function-declaration+ → Function-specification
Name-list ":" Domain ":->" Name → Function-declaration
Name-list ":" ":->" Name → Function-declaration

Variable-declaration Equation-section → Equation-specification
"var" Variables+ → Variable-declaration
Variables+ → Variable-declaration
Name-list ":" Name → Variables
Name → Data-term
Name "(" { Data-term "," }+ ")" → Data-term
"rew" Single-equation+ → Equation-section
Data-term ":=" Data-term → Single-equation

Process-term "+" Process-term → Process-term {right}
Process-term "||" Process-term → Process-term {right}
Process-term "||_" Process-term → Process-term
Process-term ":" Process-term → Process-term {right}
Process-term ":<" Data-term ":>" Process-term → Process-term
Process-term ":<" Data-term "|>" Process-term → Process-term
Process-term ":<" Data-term "|>" Process-term → Process-term {right}
Process-term ":<" Data-term "|>" Process-term → Process-term {right}
Process-term ":<" Data-term "|>" Process-term → Process-term {right}
Process-term ":<" Data-term "|>" Process-term → Process-term {left}
"delta" → Process-term
In order to relate the symbols that we use in the text, with their plain text representation in the syntax above, we provide a translation table (see Table 7). We followed some conventions regarding naming that have been set by PSF [31]. Above the double line in this table keywords and operators that occur explicitly in the syntax above. Below the double line some predefined names occur.

B Static semantics and wellformedness

In this section it is defined when a specification is correctly defined. We use the syntactical categories from the previous section (in teletype font) to refer to items in a specification. If we denote a concrete part of a specification, we prefer using the latex symbols, to increase readability. The definitions below
<table>
<thead>
<tr>
<th><strong>plain text</strong></th>
<th><strong>\LaTeX</strong></th>
</tr>
</thead>
<tbody>
<tr>
<td>sort sort</td>
<td>sort</td>
</tr>
<tr>
<td>func func</td>
<td>func</td>
</tr>
<tr>
<td>map map</td>
<td>map</td>
</tr>
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This is the symbol for time zero
This is a binary function from \texttt{Time} \times \texttt{Time} to \texttt{Bool}

Table 7: Translation table
are an adapted copy from those in [17].

In essence the static semantics says that functions and terms are well typed, and some sorts and functions are present in the specification. The validity of all static semantic requirements can efficiently be decided for any specification.

A specification is well formed, if it satisfies the static semantic requirements, the symmetric closure of the communication function is associative, there are no empty sorts and the sorts \textbf{Bool} and \textbf{Time} are appropriately defined. We only give an operational semantics to well formed specifications.

### B.1 The signature of a specification

**Definition B.1.** The signature $\text{Sig}(E)$ of a Specification $E$ consists of a seven-tuple

$$(\text{Sort}, \text{Fun}, \text{Map}, \text{Act}, \text{Comm}, \text{Proc}, \text{Init})$$

where each component is a set containing all elements of a main syntactical category declared in $E$. The signature $\text{Sig}(E)$ of $E$ is inductively defined as follows:

- If $E \equiv \text{sort } n_1 \cdots n_m$ with $m \geq 1$, then $\text{Sig}(E) \triangleq (\{n_1, \ldots, n_m\}, \emptyset, \emptyset, \emptyset, \emptyset, \emptyset)$.
- If $E \equiv \text{func } f_1 \cdots f_n$ with $n \geq 1$, then $\text{Sig}(E) \triangleq (\emptyset, \text{Fun}, \emptyset, \emptyset, \emptyset, \emptyset)$, where

\[
\begin{align*}
\text{Fun} & \triangleq \{n_{ij} : S_i \mid f_i \equiv n_{i1}, \ldots, n_{il} : S_i, 1 \leq i \leq m, 1 \leq j \leq l_i \} \\
& \cup \{n_{ij} : S_i \times \cdots \times S_i \mid \text{fun} \equiv n_{i1}, \ldots, n_{il} : S_i \times \cdots \times S_i, 1 \leq i \leq m, 1 \leq j \leq l_i \}.
\end{align*}
\]

- If $E \equiv \text{map } m_1 \cdots m_n$ with $n \geq 1$, then $\text{Sig}(E) \triangleq (\emptyset, \emptyset, \text{Map}, \emptyset, \emptyset, \emptyset, \emptyset)$, where

\[
\begin{align*}
\text{Map} & \triangleq \{n_{ij} : S_i \mid m_i \equiv n_{i1}, \ldots, n_{il} : S_i, 1 \leq i \leq m, 1 \leq j \leq l_i \} \\
& \cup \{n_{ij} : S_i \times \cdots \times S_i \mid \text{map} \equiv n_{i1}, \ldots, n_{il} : S_i \times \cdots \times S_i, 1 \leq i \leq m, 1 \leq j \leq l_i \}.
\end{align*}
\]

- If $E$ is a Equation-specification, then $\text{Sig}(E) \triangleq (\emptyset, \emptyset, \emptyset, \emptyset, \emptyset, \emptyset, \emptyset)$.
- If $E \equiv \text{act } a_1 \cdots a_n$ with $n \geq 1$, then $\text{Sig}(E) \triangleq (\emptyset, \emptyset, \emptyset, \text{Act}, \emptyset, \emptyset, \emptyset)$, where

\[
\begin{align*}
\text{Act} & \triangleq \{n_i \mid a_i \equiv n_i, 1 \leq i \leq m \} \\
& \cup \{n_{ij} : S_i \times \cdots \times S_i \mid a_i \equiv n_{i1}, \ldots, n_{il} : S_i \times \cdots \times S_i, 1 \leq i \leq m, 1 \leq j \leq l_i \}.
\end{align*}
\]

- If $E \equiv \text{comm } c_1 \cdots c_n$ with $n \geq 1$, then $\text{Sig}(E) \triangleq (\emptyset, \emptyset, \emptyset, \text{Comm}, \emptyset, \emptyset, \emptyset)$.
- If $E \equiv \text{proc } p_1 \cdots p_n$ with $n \geq 1$, then $\text{Sig}(E) \triangleq (\emptyset, \emptyset, \emptyset, \text{Proc}, \emptyset, \emptyset, \emptyset)$.
- If $E \equiv \text{init } p$ then $\text{Sig}(E) \triangleq (\emptyset, \emptyset, \emptyset, \emptyset, \emptyset, \emptyset, \{p\})$.
- If $E \equiv E_1, E_2$ with $\text{Sig}(E_i) = (\text{Sort}_i, \text{Fun}_i, \text{Map}_i, \text{Act}_i, \text{Comm}_i, \text{Proc}_i, \text{Init}_i)$ for $i = 1, 2$, then

\[
\text{Sig}(E) \triangleq (\text{Sort}_1 \cup \text{Sort}_2, \text{Fun}_1 \cup \text{Fun}_2, \text{Map}_1 \cup \text{Map}_2, \\
\text{Act}_1 \cup \text{Act}_2, \text{Comm}_1 \cup \text{Comm}_2, \text{Proc}_1 \cup \text{Proc}_2, \text{Init}_1 \cup \text{Init}_2).
\]

**Definition B.2.** Let $\text{Sig} = (\text{Sort}, \text{Fun}, \text{Map}, \text{Act}, \text{Comm}, \text{Proc}, \text{Init})$ be a signature. We write

$\text{Sig.Sort}$ for $\text{Sort}$, $\text{Sig.Fun}$ for $\text{Fun}$, $\text{Sig.Map}$ for $\text{Map}$, $\text{Sig.Act}$ for $\text{Act}$, $\text{Sig.Comm}$ for $\text{Comm}$, $\text{Sig.Proc}$ for $\text{Proc}$, $\text{Sig.Init}$ for $\text{Init}$. 
B.2 Variables

Variables play an important role in specifications. The next definition says given a specification $E$, which elements from $\text{Name}$ can play the role of a variable without confusion with defined constants. Moreover, variables must have an unambiguous and declared sort.

**Definition B.3.** Let $\text{Sig}$ be a signature. A set $\mathcal{V}$ containing pairs $\langle x; S \rangle$ with $x$ and $S$ from $\text{Name}$, is called a set of variables over $\text{Sig}$ iff for each $\langle x; S \rangle \in \mathcal{V}$:

- for each $\text{Name} S'$ and Process-term $p$ it holds that $x : \rightarrow S' \notin \text{Sig.Fun} \cup \text{Sig.Map}$, $x \notin \text{Sig.Act}$ and $x = p \notin \text{Sig.Proc}$,
- $S \in \text{Sig.Sort}$,
- for each $\text{Name} S'$ such that $S' \neq S$ it holds that $\langle x; S' \rangle \notin \mathcal{V}$.

**Definition B.4.** Let $v d$ be a Variable-declaration. The function $\text{Vars}$ is defined by:

\[
\text{Vars}(v d) \overset{\text{def}}{=} \begin{cases} \emptyset & \text{if } v d \text{ is empty,} \\ \{(x_i; S_i) \mid 1 \leq i \leq m, \\ \quad 1 \leq j \leq l_i \} & \text{if } v d \equiv \text{var} x_{11}, \ldots, x_{1t_1}; S_{11} \ldots x_{m1}, \ldots, x_{ml_m}; S_{ml_m}. \end{cases}
\]

In the following definitions we give functions yielding the sort of and the variables in a Data-term $t$.

**Definition B.5.** Let $t$ be a Data-term and $\text{Sig}$ a signature. Let $\mathcal{V}$ be a set of variables over $\text{Sig}$. We define:

\[
\text{sort}_{\text{Sig}, \mathcal{V}}(t) \overset{\text{def}}{=} \begin{cases} S & \text{if } t \equiv x \text{ and } \langle x; S \rangle \in \mathcal{V}, \\ S & \text{if } t \equiv n, n: \rightarrow S \in \text{Sig.Fun} \cup \text{Sig.Map} \\ \quad \text{and for no } S' \neq S \text{ : } n: \rightarrow S' \in \text{Sig.Fun} \cup \text{Sig.Map}, \\ S & \text{if } t \equiv n(t_1, \ldots, t_m), \\ \quad n; \text{sort}_{\text{Sig}, \mathcal{V}}(t_1) \times \cdots \times \text{sort}_{\text{Sig}, \mathcal{V}}(t_m) \rightarrow S \in \text{Sig.Fun} \cup \text{Sig.Map} \\ \quad \text{and for no } S' \neq S \text{ : } n; \text{sort}_{\text{Sig}, \mathcal{V}}(t_1) \times \cdots \times \text{sort}_{\text{Sig}, \mathcal{V}}(t_m) \rightarrow S' \in \text{Sig.Fun} \cup \text{Sig.Map}, \\ \bot & \text{otherwise.} \end{cases}
\]

If a variable or a function is not or inappropriately declared no answer can be obtained. In this case $\bot$ results.

**Definition B.6.** Let $\text{Sig}$ be a signature, $\mathcal{V}$ a set of variables over $\text{Sig}$ and $t$ be a Data-term.

\[
\text{Var}_{\text{Sig}, \mathcal{V}}(t) \overset{\text{def}}{=} \begin{cases} \{\langle x; S \rangle\} & \text{if } t \equiv x \text{ and } \langle x; S \rangle \in \mathcal{V}, \\ \emptyset & \text{if } t \equiv n \text{ and } n: \rightarrow S \in \text{Sig.Fun} \cup \text{Sig.Map}, \\ \bigcup_{1 \leq i \leq m} \text{Var}_{\text{Sig}, \mathcal{V}}(t_i) & \text{if } t \equiv n(t_1, \ldots, t_m), \\ \{\bot\} & \text{otherwise.} \end{cases}
\]

We call a Data-term $t$ closed wrt. a signature $\text{Sig}$ and a set of variables $\mathcal{V}$ iff $\text{Var}_{\text{Sig}, \mathcal{V}}(t) = \emptyset$. Note that $\text{Var}_{\text{Sig}, \mathcal{V}}(t) \subseteq \mathcal{V} \cup \{\bot\}$ for any Data-term $t$. If $\bot \in \text{Var}_{\text{Sig}, \mathcal{V}}(t)$, then due to some missing or inappropriate declaration it cannot be determined what the variables of $t$ are on basis of $\text{Sig}$ and $\mathcal{V}$.

B.3 Static semantics

A Specification must be internally consistent. This means that all objects that are used must be declared exactly once and are used such that the sorts are correct. It also means that action, process, constant and variable names cannot be confused. Furthermore, it means that communications are specified in a functional way and that it is guaranteed that the terms used in an equation are well-typed. Because all these properties can be statically decided, a specification that is internally consistent is called SSC (Statically Semantically Correct). All next definitions culminate in Definition B.13.
Definition B.7 (Data-term). Let $\text{Sig}$ be a signature, and let $\mathcal{V}$ be a set of variables over $\text{Sig}$. A data-term $t$ is called SSC wrt. $\text{Sig}$ and $\mathcal{V}$ iff one of the following holds:

- $t \equiv n$ with $n$ a Name and $(n; S) \in \mathcal{V}$ for some $S$, or $n; \rightarrow \text{sort}_{\text{Sig}, \mathcal{V}}(n) \in \text{Sig}.\text{Fun} \cup \text{Sig}.\text{Map}$.
- $t \equiv n(t_1, \ldots, t_m)$ ($m \geq 1$) and $n; \text{sort}_{\text{Sig}, \mathcal{V}}(t_1) \times \cdots \times \text{sort}_{\text{Sig}, \mathcal{V}}(t_m) \rightarrow \text{sort}_{\text{Sig}, \mathcal{V}}(n(t_1, \ldots, t_m)) \in \text{Sig}.\text{Fun} \cup \text{Sig}.\text{Map}$ and all $t_i$ ($1 \leq i \leq m$) are SSC wrt. $\text{Sig}$ and $\mathcal{V}$.

Definition B.8 (Equation-section). Let $\text{Sig}$ be a signature and $\mathcal{V}$ be a set of variables over $\text{Sig}$. An equation-section rew $\text{rw}_1 \cdots \text{rw}_m$ with $m \geq 1$ is SSC wrt. $\text{Sig}$ and $\mathcal{V}$ iff for all $1 \leq i \leq m$ if $\text{rw}_i \equiv \text{t}_1 = \text{t}_2$, both $\text{t}_1$ and $\text{t}_2$ are SSC wrt. $\text{Sig}$ and $\mathcal{V}$ and $\text{sort}_{\text{Sig}, \mathcal{V}}(\text{t}_1) = \text{sort}_{\text{Sig}, \mathcal{V}}(\text{t}_2)$.

Definition B.9 (Variable-declaration). A variable-declaration vd is SSC wrt. a signature $\text{Sig}$ iff one of the following holds:

- $vd$ is empty.
- $vd \equiv \text{var} \ n_{11}, \ldots, n_{1k_1}; S_1 \ldots n_{m1}, \ldots, n_{mk_m}; S_m$ with $m \geq 1$, $k_i \geq 1$ for $1 \leq i \leq m$ and
  - $n_{ij} \neq n_{i'j'}$ whenever $i \neq i'$ or $j \neq j'$ for $1 \leq i \leq m$, $1 \leq i' \leq m$, $1 \leq j \leq k_i$ and $1 \leq j' \leq k_{i'}$,
  - the set $\text{Vars}(\text{var} \ n_{11}, \ldots, n_{1k_1} : S_1 \ldots n_{m1}, \ldots, n_{mk_m} : S_m)$ is a set of variables over $\text{Sig}$.

Definition B.10 (Process-term). Let $\text{Sig}$ be a signature and $\mathcal{V}$ be a set of variables over $\text{Sig}$. We say that a process-term $p$ is SSC wrt. to $\text{Sig}$ and $\mathcal{V}$ iff one of the following holds:

- $p \equiv p_1 + p_2$, $p \equiv p_1 \parallel p_2$, $p \equiv p_1 \mid p_2$, $p \equiv p_1 \mid p_2$, $p \equiv p_1 \& p_2$ or $p \equiv p_1 \& p_2$ and
  - $p_1$ is SSC wrt. $\text{Sig}$ and $\mathcal{V}$,
  - $p_2$ is SSC wrt. $\text{Sig}$ and $\mathcal{V}$.
- $p \equiv p_1 \text{ at } p_2$ and
  - $p_1$ is SSC wrt. $\text{Sig}$ and $\mathcal{V}$,
  - $p_2$ is SSC wrt. $\text{Sig}$ and $\mathcal{V}$,
  - $t$ is SSC wrt. $\text{Sig}$ and $\mathcal{V}$ and $\text{sort}_{\text{Sig}, \mathcal{V}}(t) = \text{Bool}$.
- $p \equiv p_1; t$ and
  - $p_1$ is SSC wrt. $\text{Sig}$ and $\mathcal{V}$
  - $t$ is SSC wrt. $\text{Sig}$ and $\mathcal{V}$ and $\text{sort}_{\text{Sig}, \mathcal{V}}(t) = \text{Time}$.
- $p \equiv \delta$ or $p \equiv \tau$.
- $p \equiv \delta_{\{n_1, \ldots, n_m\}} p_1$ or $p \equiv \tau_{\{n_1, \ldots, n_m\}} p_1$ with $m \geq 1$ and
  - for all $1 \leq i < j \leq m$ $n_i \neq n_j$,
  - for $1 \leq i \leq m$ either $n_i \in \text{Sig}.\text{Act}$ or $n_i; S_1 \times \cdots \times S_k \in \text{Sig}.\text{Act}$ for some $k \geq 1$ and Names $S_1, \ldots, S_k$,
  - $p_1$ is SSC wrt. $\text{Sig}$ and $\mathcal{V}$.
- $p \equiv \rho_{\{n_1 \rightarrow n'_1, \ldots, n_m \rightarrow n'_m\}} p_1$ and
– for \( 1 \leq i \leq m \) either \( n_i \in \text{Sig.Act} \) or \( n_i : S_i \times \cdots \times S_k \in \text{Sig.Act} \) for some \( k \geq 1 \) and Names \( S_1, \ldots, S_k \),
– for each \( 1 \leq i < j \leq m \) it holds that \( n_i \not\equiv n_j \),
– for \( 1 \leq i \leq m, k \geq 1 \) and Names \( S_1, \ldots, S_k \) it holds that if \( n_i : S_i \times \cdots \times S_k \in \text{Sig.Act} \), then also \( n'_i : S_i \times \cdots \times S_k \in \text{Sig.Act} \),
– for \( 1 \leq i \leq m \) it holds that if \( n_i \in \text{Sig.Act} \), then also \( n'_i \in \text{Sig.Act} \),
– \( p_1 \) is SSC wrt \( \text{Sig} \) and \( \mathcal{V} \).

\( p \equiv \Sigma x : p_n \) and iff

– \((\forall \{(x:S') | S' \) is a Name\}) \cup \{(x:S)\}\) is a set of variables over \( \text{Sig} \),
– \( p_1 \) is SSC wrt \( \text{Sig} \) and \((\forall \{(x:S') | S' \) is a Name\}) \cup \{(x:S)\}\).

\( p \equiv n \) and \( n \equiv p' \in \text{Sig.Proc} \) for some \( \text{Process-term} \) \( p' \), or \( n \in \text{Sig.Act} \).

\( p \equiv n(t_1, \ldots, t_m) \) with \( m \geq 1 \) and

– \( n(x_1, \text{sort}_{\text{Sig}, y(t_1)}, \ldots, x_m, \text{sort}_{\text{Sig}, y(t_m)}) = p' \in \text{Sig.Proc} \) for Names \( x_1, \ldots, x_m \) and Process-term \( p' \), or \( n; \text{sort}_{\text{Sig}, y(t_1)} \times \cdots \times \text{sort}_{\text{Sig}, y(t_m)} \in \text{Sig.Act} \),
– for \( 1 \leq i \leq m \) the Data-term \( t_i \) is SSC wrt \( \text{Sig} \) and \( \mathcal{V} \).

**Definition B.11 (Action-declaration).** Let \( \text{Sig} \) be a signature. An Action-declaration \( ad \) is SSC wrt \( \text{Sig} \) iff one of the following hold:

– \( ad \equiv n \) and for each Name \( S' \) it holds that \( n; \rightarrow S' \not\in \text{Sig.Fun} \cup \text{Sig.Map} \).

– An Action-declaration \( n_1, \ldots, n_m : S_1 \times \cdots \times S_k \) with \( k, m \geq 1 \) is SSC wrt \( \text{Sig} \) iff

  – for all \( 1 \leq i < j \leq m \) it holds that \( n_i \not\equiv n_j \),
  – for all \( 1 \leq i \leq k \) it holds that \( S_i \in \text{Sig.Sort} \),
  – for all \( 1 \leq i \leq m \) and for each Name \( S' \) it holds that \( n_i : S_i \times \cdots \times S_k \rightarrow S' \not\in \text{Sig.Fun} \cup \text{Sig.Map} \).

**Definition B.12 (Specification).** Let \( \text{Sig} \) be a signature and \( \mathcal{V} \) be a set of variables over \( \text{Sig} \). We define the predicate 'is SSC wrt \( \text{Sig} \)' inductively over the syntax of a Specification.

– A Specification \( \text{sort} \) \( n_1 \cdots n_m \) with \( m \geq 1 \) is SSC wrt \( \text{Sig} \) iff all \( n_1, \ldots, n_m \) are pairwise different.

– A Specification \( \text{func} \) \( n_{11}, \ldots, n_{1i_1}: S_{11} \times \cdots \times S_{1k_1} \rightarrow S_1 \)

  \[ \vdots \]

  \( n_{m1}, \ldots, n_{m_i_m}: S_{m_1} \times \cdots \times S_{m_k_m} \rightarrow S_m \)

  with \( m \geq 1, l_i \geq 1, k_i \geq 0 \) for \( 1 \leq i \leq m \) is SSC wrt \( \text{Sig} \) iff

  – for all \( 1 \leq i \leq m \) \( n_{1i}, \ldots, n_{li} \), are pairwise different,
  – for all \( 1 \leq i < j \leq m \) it holds that if \( n_{ik} \equiv n_{jk} \) for some \( 1 \leq k \leq l_i \) and \( 1 \leq k' \leq l_j \), then either \( k_i \not\equiv k_j \) or \( S_{ii} \not\equiv S_{jj} \) for some \( 1 \leq l \leq k_i \),
  – for all \( 1 \leq i \leq m \) and \( 1 \leq j \leq k_i \) it holds that \( S_{ij} \in \text{Sig.Sort} \) and \( S_i \in \text{Sig.Sort} \).
• A Specification $\text{map} \ n_{i1}, \ldots, n_{ik_i} : S_{i1} \times \cdots \times S_{ik_i} \to S_i$

\[
\vdots
\]

\[
n_{m1}, \ldots, n_{mk_m} : S_{m1} \times \cdots \times S_{mk_m} \to S_m
\]

with $m \geq 1$, $l_i \geq 1$, $k_i \geq 0$ for $1 \leq i \leq m$ is SSC wrt. $\text{Sig}$ iff

- for all $1 \leq i \leq m$ $n_{i1}, \ldots, n_{ik_i}$ are pairwise different,
- for all $1 \leq i < j \leq m$ it holds that if $n_{ik} \equiv n_{jk'}$ for some $1 \leq k \leq l_i$ and $1 \leq k' \leq l_j$, then either $k_i \neq k_j$, or $S_{ki} \neq S_{kj}$ for some $1 \leq l \leq k_i$,
- for all $1 \leq i \leq m$ and $1 \leq j \leq k_i$ it holds that $S_{ij} \in \text{Sig.Sort}$ and $S_i \in \text{Sig.Sort}$.

• A Specification of the form: $\text{var-dec} \\
\text{rew-rul}$

where $\text{var-dec}$ is a Variable-declaration and $\text{rew-rul}$ is an Equation-section is SSC wrt. $\text{Sig}$ iff

- $\text{var-dec}$ is SSC wrt. $\text{Sig}$,
- $\text{rew-rul}$ is SSC wrt. $\text{Sig}$ and $\text{Vars(\text{var-dec})}$. 

• A Specification $\text{act} \ a_d_1 \cdots a_d_m$ with $m \geq 1$ is SSC wrt. $\text{Sig}$ iff

- for all $1 \leq i \leq m$ the Action-declaration $a_d_i$ is SSC wrt. $\text{Sig}$,
- for all $1 \leq i < j \leq m$ it holds that $\text{Sig(\text{act } a_d_i}. \text{Act } \cap \text{Sig(\text{act } a_d_j}). \text{Act } = \emptyset.

• A Specification $\text{comm} \ n_{i1}|n_{i2} = n_{i3} \cdots n_{ik} = n_{im}$ with $m \geq 1$ is SSC wrt. $\text{Sig}$ iff

- for each $1 \leq i < j \leq m$ it is not the case that $n_{i1} \equiv n_{j1}$ and $n_{i2} \equiv n_{j2}$, or $n_{i1} \equiv n_{j2}$ and $n_{i2} \equiv n_{j1}$,
- for each $1 \leq i \leq m$ either $n_{i1} \in \text{Sig.Act}$ or there is a $k \geq 1$ such that $n_{i1}:S_1 \times \cdots \times S_k \in \text{Sig.Act}$,
- for each $1 \leq i \leq m$, $k \geq 1$ and Names $S_1, \ldots, S_k$ it holds that if $n_{i1}:S_1 \times \cdots \times S_k \in \text{Sig.Act}$ then $n_{i2}:S_1 \times \cdots \times S_k \in \text{Sig.Act}$ and $n_{i3}:S_1 \times \cdots \times S_k \in \text{Sig.Act}$,
- for each $1 \leq i \leq m$ it holds that if $n_{i1} \in \text{Sig.Act}$ then $n_{i2} \in \text{Sig.Act}$ and $n_{i3} \in \text{Sig.Act}$,
- for each $1 \leq i \leq m$ it holds that if $n_{i2} \in \text{Sig.Act}$ then $n_{i1} \in \text{Sig.Act}$ and $n_{i3} \in \text{Sig.Act}$.

• A specification $\text{proc} \ p_d_1 \cdots p_d_m$ with $m \geq 1$ is SSC wrt. $\text{Sig}$ iff

- for each $1 \leq i < j \leq m$:

  * if $p_{di} \equiv n = p$ and $p_{dj} \equiv n' = p'$ then $n \neq n'$,
  * if for some $k \geq 1$ it holds that $p_{di} \equiv n(x_1:S_1, \ldots, x_k:S_k) = p$ and $p_{dj} \equiv n'(x_1':S_1, \ldots, x_k':S_k) = p'$ then $n \neq n'$,

- if $p_{di} \equiv n = p \ (1 \leq i \leq m)$, then

  * $n \notin \text{Sig.Act}$ and $p$ is SSC wrt. $\text{Sig}$ and $\emptyset$, and
  * for each Name $S'$ it holds that $n : \to S' \notin \text{Sig.Fun } \cup \text{Sig.Map}$,

- if $p_{di} \equiv n(x_1:S_1, \ldots, x_k:S_k) = p \ (1 \leq i \leq m)$, then

  * $n:S_1 \times \cdots \times S_k \notin \text{Sig.Act}$,
  * for each Name $S'$ it holds that $n:S_1 \times \cdots \times S_k \to S' \notin \text{Sig.Fun } \cup \text{Sig.Map}$,
the Names $x_1, \ldots, x_k$ are pairwise different and $\{\langle x_j ; S_j \rangle \mid 1 \leq j \leq k \}$ is a set of variables over $\text{Sig}$,

* $p$ is SSC wrt. $\text{Sig}$ and $\{\langle x_j ; S_j \rangle \mid 1 \leq j \leq k \}$.

- A specification of the form $\text{init } p$ is SSC wrt. $\text{Sig}$ iff $p$ SSC wrt. to $\text{Sig}$ and $\emptyset$.

- A specification $E_1, E_2$ is SSC wrt. $\text{Sig}$ iff
  
  - $E_1$ and $E_2$ are SSC wrt. $\text{Sig}$,
  
  - $\text{Sig}(E_1).\text{Sort} \cap \text{Sig}(E_2).\text{Sort} = \emptyset$,
  
  - if $n : S_1 \times \cdots \times S_m \rightarrow S \in \text{Sig}(E_1).\text{Fun} \cup \text{Sig}(E_1).\text{Map}$ for some $m \geq 0$ then $n : S_1 \times \cdots \times S_m \rightarrow S' \notin \text{Sig}(E_2).\text{Fun} \cup \text{Sig}(E_2).\text{Map}$ for any Name $S'$,
  
  - $\text{Sig}(E_1).\text{Act} \cap \text{Sig}(E_2).\text{Act} = \emptyset$,
  
  - if $n_1|n_2 = n_3 \in \text{Sig}(E_1).\text{Comm}$ then for any Names $n_1'$ and $n_3'$ $n_1|n_2 = n_3' \notin \text{Sig}(E_2).\text{Comm}$ and $n_2|n_1 = n_3' \notin \text{Sig}(E_2).\text{Comm}$,
  
  - if $\text{pd}_1 \in \text{Sig}(E_1).\text{Proc}$ and $\text{pd}_2 \in \text{Sig}(E_2).\text{Proc}$, then
    
    * if $\text{pd}_1 \equiv n_1 = p_1$ and $\text{pd}_2 \equiv n_2 = p_2$, then $n_1 \neq n_2$,
    
    * if for some $m \geq 1$ $\text{pd}_1 \equiv n_1(x_1:S_1, \ldots, x_m:S_m) = p_1$ and $\text{pd}_2 \equiv n_2(x_1':S_1, \ldots, x_m':S_m) = p_2$, then $n_1 \neq n_2$,
  
  - $\text{Sig}(E_1).\text{Init} = \emptyset$ or $\text{Sig}(E_2).\text{Init} = \emptyset$.

**Definition B.13.** Let $E$ be a specification. We say that $E$ is SSC iff $E$ is SSC wrt. $\text{Sig}(E)$.

#### B.4 The communication function

The following definition helps us in guaranteeing that the communication function is commutative and associative. This implies that the merge is also commutative and associative which allows us to write parallel processes without brackets.

**Definition B.14.** Let $\text{Sig}$ be a signature. The set $\text{Sig}.\text{Comm}^*$ is defined by:

\[
\text{Sig}.\text{Comm}^* \overset{\text{def}}{=} \{ n_1|n_2 = n_3, n_2|n_1 = n_3 | n_1|n_2 = n_3 \in \text{Sig}.\text{Comm} \}.
\]

So, in $\text{Sig}.\text{Comm}^*$ communication is always commutative. A specification $E$ is communication-associative iff

\[
n_1|n_2 = n, n|n_3 = n' \in \text{Sig}(E).\text{Comm}^* \Rightarrow \exists n'' : n_2|n_3 = n'', n_1|n'' = n' \in \text{Sig}(E).\text{Comm}^*.
\]

With the condition that $E$ is SSC this exactly implies that communication is associative.

#### B.5 Well-formed μCRL specifications

We define what well-formed specifications are. We only provide well-formed specifications with a semantics. Well-formedness is a decidable property.

**Definition B.15.** Let $\text{Sig}$ be a signature. We call a Name $S$ a constructor sort iff $S \in \text{Sig}.\text{Sort}$ and there exists Names $S_1, \ldots, S_k$, $f$ ($k \geq 0$) such that $f:S_1 \times \cdots \times S_k \rightarrow S \in \text{Sig}.\text{Fun}$.

**Definition B.16.** Let $E$ be a specification that is SSC. We inductively define which sorts are non empty constructor sorts in $E$. A constructor sort $S$ is called non empty iff there is a function $f:S_1 \times \cdots \times S_k \rightarrow S \in \text{Sig}.\text{Fun}$ ($k \geq 0$) such that for all $1 \leq i \leq k$ if $S_i$ is a constructor sort, it is non empty. We say that $E$ has no empty constructor sorts iff each constructor sort is non empty.
Definition B.17. Let $E$ be a Specification. $E$ is called well-formed iff

- $E$ is SSC,
- $E$ is communication-associative,
- $E$ has no empty constructor sorts,
- $\text{Bool} \in \text{Sig}(E).\text{Sort}, T : \rightarrow \text{Bool} \in \text{Sig}(E).\text{Fun}$ and $F : \rightarrow \text{Bool} \in \text{Sig}(E).\text{Fun},$
- If $\text{Time} \in \text{Sig}(E).\text{Sort}$, then $0 : \rightarrow \text{Time} \in \text{Sig}(E).\text{Fun} \cup \text{Sig}(E).\text{Map}$ and $\leq : \text{Time} \times \text{Time} \rightarrow \text{Bool} \in \text{Sig}(E).\text{Map}.$

C  Semantics of the datatypes

In this section we define the models of the data part of well-formed specifications. Given a signature $\text{Sig}$ we introduce the class of $\text{Sig}$-algebras. For a well-formed Specification $E$ with $\text{Sig}(E) = \text{Sig}$, we define the subclass of $\text{Sig}$-algebras that form a model for the data part of $E$. As we want to denote all elements in an algebra, we extend the signature with constants. But first we introduce substitutions.

C.1 Substitutions

We define substitutions on Data-terms and Process-terms.

Definition C.1. Let Sig be a signature and $\mathcal{V}, \mathcal{W}$ sets of variables over Sig. A substitution $\sigma$ over Sig, $\mathcal{V}$ and $\mathcal{W}$ is a mapping from $\mathcal{V}$ to Data-terms that are SSC wrt. Sig and $\mathcal{W}$ such that for each $\langle x:S \rangle \in \mathcal{V}$ it holds that $\text{sort}_{\text{Sig}, \mathcal{W}}(\sigma(\langle x:S \rangle)) = S$. Substitutions are extended to Data-terms that are SSC wrt. Sig and $\mathcal{V}$ by:

$$
\sigma(x) \overset{\text{def}}{=} \sigma(\langle x:S \rangle) \quad \text{if } \langle x:S \rangle \in \mathcal{V} \text{ for some Name } S,
$$
$$
\sigma(n) \overset{\text{def}}{=} n \quad \text{if } n : \rightarrow S \in \text{Sig.Fun} \cup \text{Sig.Map},
$$
$$
\sigma(n(t_1, \ldots, t_m)) \overset{\text{def}}{=} n(\sigma(t_1), \ldots, \sigma(t_m)).
$$

Note that $\sigma(t)$ is SSC wrt. Sig and $\mathcal{W}$.

Definition C.2. Let Sig be a signature and $\mathcal{V}, \mathcal{W}$ sets of variables over Sig. Let $\sigma$ be a substitution over Sig, $\mathcal{V}$ and $\mathcal{W}$. We inductively extend $\sigma$ to a Process-terms that are SSC wrt. Sig and $\mathcal{V}$ as follows:

- $\sigma(p_1 \Box p_2) \overset{\text{def}}{=} \sigma(p_1) \Box \sigma(p_2)$ if $\Box \in \{+, ||, \|, ;, \ll, \leq\},$
- $\sigma(p_1 \triangleright p_2) \overset{\text{def}}{=} \sigma(p_1) \triangleright \sigma(p_2),$
- $\sigma(\triangleright t) \overset{\text{def}}{=} \sigma(t),$
- $\sigma(\triangleright) \overset{\text{def}}{=} \delta,$
- $\sigma(\triangleright) \overset{\text{def}}{=} \tau,$
- $\sigma(\Box_H(p)) \overset{\text{def}}{=} \Box_H(\sigma(p))$ if $\Box \in \{\triangleright, \triangleright, \Box\}.$
C  SEMANTICS OF THE DATATYPES 34

\[
\sigma(\Sigma_{d,DP}) \overset{\text{def}}{=} \Sigma_{e,DP}\sigma'(p) \text{ where } \sigma' \text{ is a substitution over } Sig, \mathcal{V} \cup \{(e;D)\} \text{ and } \mathcal{W} \text{ defined by}
\]

\[
\sigma'(\langle x;S \rangle) = \begin{cases} 
\sigma(\langle x;S \rangle) & \text{for all } \langle x;S \rangle \in \mathcal{V}, \\
\epsilon & \text{if } x = d \text{ and } S = D.
\end{cases}
\]

Here \( e \) is a fresh variable, i.e. for any Name \( S \) \( \langle e;S \rangle \notin \mathcal{V} \cup \mathcal{W} \) and \( \mathcal{V} \cup \{(e;D)\} \) is a set of variables over \( Sig \).

- \( \sigma(n(t_1, \ldots, t_m)) \overset{\text{def}}{=} n(\sigma(t_1), \ldots, \sigma(t_m)) \), and
- \( \sigma(n) \overset{\text{def}}{=} n \).

C.2 Equations

We define the function \( eqns \) that extracts the equations together with declared variables from a Specification.

**Definition C.3.** We define the function \( eqns \) on a Specification \( E \) inductively as follows:

- If \( E \) is a Sort-specification, Function-specification, Action-specification, Communication-specification, Process-specification or an Initial-declaration then
  \[
  eqns(E) \overset{\text{def}}{=} \emptyset.
  \]
- If \( E \equiv V R \) with \( V \) a Variable-declaration and \( R \equiv \text{rew} \; rd_1 \cdots rd_m \) an Equation-section for some \( m \geq 1 \), then
  \[
  eqns(E) \overset{\text{def}}{=} \{\{rd_i, Vars(V)\} \mid 1 \leq i \leq m\}.
  \]
- If \( E \equiv E_1 \; E_2 \) where \( E_1 \) and \( E_2 \) are Specifications, then
  \[
  eqns(E) \overset{\text{def}}{=} eqns(E_1) \cup eqns(E_2).
  \]

C.3 Algebras

**Definition C.4.** Let \( E \) be a well-formed Specification and let \( \mathbb{S} \supseteq Sig(E) . \mathbb{S} \) be a set of Names. A \( Sig(E) \)-algebra is a tuple \( \mathbb{A} = (\{D_S | S \in \mathbb{S}\}, I) \) where

- \( \{D_S | S \in \mathbb{S}\} \) is a collection of non empty sets,
- \( I \) is a function from \( Sig(E) . \mathbb{S}_n \cup Sig(E) . \mathbb{S}_n \).Map to functions over \( \bigcup_{S \in \mathbb{S}_n} D_S \) such that for every \( f : S_1 \times \cdots \times S_n \to S \in Sig(E) . \mathbb{S}_n \cup Sig(E) . \mathbb{S}_n \).Map, the function \( I(f : S_1 \times \cdots \times S_n \to S) \) runs from \( D_{S_1} \times \cdots \times D_{S_n} \) to \( D_S \), \( I(f : S \to S) \) is called the interpretation of the function \( f \).

We define the interpretation \( [\cdot]_\mathbb{A} \) from Data-terms that are SSC wrt. \( Sig(E) \) and \( \emptyset \) to the domains of \( \mathbb{A} \) as follows:

- If \( t \equiv n \), then \( [t]_\mathbb{A} \overset{\text{def}}{=} I(n : \text{sort}_{Sig(E)} \emptyset (n)) \),
- If \( t \equiv n(t_1, \ldots, t_m) \) for some \( m \geq 1 \), then \( [t]_\mathbb{A} \overset{\text{def}}{=} I(n : \text{sort}_{Sig(E)} \emptyset (t_1) \times \cdots \times \text{sort}_{Sig(E)} \emptyset (t_m) \to \text{sort}_{Sig(E)} \emptyset (t)) ([t_1]_\mathbb{A}, \ldots, [t_m]_\mathbb{A}) \).

For Data-terms \( t \) and \( u \) that are SSC wrt. \( E \) and \( \emptyset \) we write \( \mathbb{A} \models t = u \) iff \( [t]_\mathbb{A} = [u]_\mathbb{A} \).
C.4 Extensions of signatures and algebras

Definition C.5. Let $E$ be a well formed Specification, $\text{Sorts}$ a set of Names and $\mathbf{A} = \{\{D_S|S \in \text{Sorts}\}, I\}$ a model of $E$. The extended signature $\text{Sig}(E, \mathbf{A})$ is defined as follows:

- $\text{Sig}(E, \mathbf{A}).\text{Sort} = \text{Sig}(E).\text{Sort}$,
- $\text{Sig}(E, \mathbf{A}).\text{Fun} = \text{Sig}(E).\text{Fun} \cup \{c_u : \rightarrow S|u \in D_s, S \in \text{Sorts}\}$ where $c_u$ are fresh names, i.e. $c_u : \rightarrow S \notin \text{Sig}(E).\text{Fun} \cup \text{Sig}(E).\text{Map}$ for any Name $S$ and $c_u \notin \text{Sig}(E).\text{Act}$ and for all Process terms $p \ c_u = p \notin \text{Sig}(E).\text{Proc}$,
- $\text{Sig}(E, \mathbf{A}).\text{Map} = \text{Sig}(E).\text{Map}$,
- $\text{Sig}(E, \mathbf{A}).\text{Act} = \text{Sig}(E).\text{Act}$,
- $\text{Sig}(E, \mathbf{A}).\text{Comm} = \text{Sig}(E).\text{Comm}$,
- $\text{Sig}(E, \mathbf{A}).\text{Proc} = \text{Sig}(E).\text{Proc}$,
- $\text{Sig}(E, \mathbf{A}).\text{Init} = \text{Sig}(E).\text{Init}$.

The extension of an algebra $\mathbf{A}$ is the algebra $\mathbf{A}_{ext} = \{\{D_S|S \in \text{Sorts}\}, I^{'}\}$ where $I^{'}$ is defined by

$I^{'}(f:S_1 \times \cdots \times S_n \rightarrow S) = \begin{cases} u & \text{if } f = c_u \text{ and } n = 0 \\ I(f:S_1 \times \cdots \times S_n \rightarrow S) & \text{otherwise.} \end{cases}$

C.5 Model

Definition C.6. Let $E$ be a well-formed Specification and $\text{Sorts} \supseteq \text{Sig}(E).\text{Sort}$ a set of Names. A $\text{Sig}(E)$-algebra $\mathbf{A} = \{\{D_S|S \in \text{Sorts}\}, I\}$ is a model of $E$, notation $\mathbf{A} \models E$, iff

1. whenever $(t = t', \forall) \in \text{eqns}(E)$, then for any substitution $\sigma$ over $\text{Sig}(E, \mathbf{A})$, $\forall$ and $\emptyset$ it holds that $\mathbf{A}_{ext} \models \sigma(t) = \sigma(t')$.
2. if $S \in \text{Sig}(E).\text{Sort}$ is a constructor sort, then for every element $u \in D_S$ there is a function $f:S_1 \times \cdots \times S_m \rightarrow S \in \text{Sig}(E).\text{Fun}$ such that $u = I(f:S_1 \times \cdots \times S_m \rightarrow S)(a_1, \ldots, a_m)$ with $a_i \in D_{S_i}$.
3. The set $D_{\text{Bool}}$ contains exactly two elements, and $I(t: \rightarrow \text{Bool})$ and $I(f: \rightarrow \text{Bool})$ are different.
4. The Name $\text{Time} \in \text{Sorts}$ and there exists functions $0_\mathbf{A}: \rightarrow D_{\text{Time}}$ and $\leq_\mathbf{A} : D_{\text{Time}} \times D_{\text{Time}} \rightarrow D_{\text{Bool}}$ such that
   - if $t_1 \leq_\mathbf{A} t_2 = t_3$ and $t_2 \leq_\mathbf{A} t_3$ then $t_1 \leq_\mathbf{A} t_3 = t_\mathbf{A}$ for all $t_1, t_2, t_3 \in D_{\text{Time}}$,
   - $0_\mathbf{A} \leq_\mathbf{A} t = t_\mathbf{A}$ for all $t \in D_{\text{Time}}$,
   - Either $t_1 \leq_\mathbf{A} t_2$ or $t_2 \leq_\mathbf{A} t_1$ for all $t_1, t_2 \in D_{\text{Time}}$,
   - for all $t_1, t_2 \in D_{\text{Time}}$ $t_1 \leq_\mathbf{A} t_2 \rightarrow t_\mathbf{A}$ and $t_2 \leq_\mathbf{A} t_1 = t_\mathbf{A}$ if $t_1 = t_2$.

Here $t_\mathbf{A} = I(t: \rightarrow \text{Bool})$. Moreover, if $\text{Time} \in \text{Sig}(E).\text{Sort}$, then $0_\mathbf{A} = I(0: \rightarrow \text{Time})$ and $\leq_\mathbf{A} = I(\leq : \text{Time} \times \text{Time} \rightarrow \text{Time})$.

D Operational semantics

In this section we assume that a well-formed specification $E$ with some timed $\mu$CRL model $\mathbf{A}$ are given. We define for each process-term $p$ that is SSC wrt. $\text{Sig}(E)$ and $\emptyset$, a transition system, explaining the operational behaviour of such a term.
D.1 The delay predicate

The delay predicate \( U(t, p) \) expresses that \( p \) can at least idle until time \( t \). It has the same purpose as the ultimate delay in \([1, 2, 27]\).

**Definition D.1.** Let \( E \) be a well-formed Specification that is SSC and \( \text{Sorts} \supseteq \text{Sig}(E), \text{Sort} \) a set of Names. Let \( A = (\{D_S | S \in \text{Sorts}\}, \Gamma) \) be a model of \( E \). Let \( p \) be a Process-term that is SSC wrt. \( \text{Sig}(E, A) \) and \( \emptyset \), and \( t \in D_{\text{Time}} \). The ultimate delay \( U(t, p) \) is the least predicate satisfying:

\[
\begin{align*}
U(t, a), U(t, a(u_1, \ldots, u_n)), U(t, \tau) \text{ and } U(t, \delta) \text{ hold; } \\
U(t, p + q) \text{ holds iff } U(t, p) \text{ or } U(t, q); \\
U(t, p q) \text{ holds iff } U(t, p); \\
U(t, p \text{ } c \text{ } q) \text{ holds iff } U(t, p) \text{ and } A_{\text{ext}} \vdash c = t, \text{ or } U(t, q) \text{ and } A_{\text{ext}} \vdash c = t; \\
U(t, \sum_{i,S} p) \text{ holds iff for some } u \in D_S \text{ } \sigma(u) \text{ over } \text{Sig}(E, A), \{\langle d:S \rangle\} \text{ and } \emptyset \text{ such that } \sigma(d) = u; \\
U(t, p R(p)); U(t, \tau I(p)) \text{ and } U(t, \partial_H(p)) \text{ hold iff } U(t, p); \\
U(t, p \parallel q), U(t, p | q), U(t, p \downarrow q) \text{ and } U(t, p \ll q) \text{ hold iff } U(t, p) \text{ and } U(t, q); \\
U(t, p t') \text{ holds iff } t \leq A [t']^A_{\text{ext}} \text{ and } U(t, p); \\
U(t, X) \text{ holds iff } U(t, p) \text{ and } X = p \in \text{Sig}(E).\text{proc}; \\
U(t, X(u_1, \ldots, u_n)) \text{ holds iff } U(t, \sigma(p)) \text{ where } \sigma \text{ is a substitution over } \text{Sig}(E, A), \{\langle x_i:S_i \rangle \mid 1 \leq i \leq n\} \text{ and } \emptyset \text{ such that } \sigma(\langle x_i:S_i \rangle) = u_i, \text{ and } X(x_1:S_1, \ldots, x_n:S_n) = p \in \text{Sig}(E).\text{proc} \text{ and } \text{sort}_{\text{Sig}, \emptyset}(u_k) = S_i.
\end{align*}
\]

D.2 Transition system (general)

**Definition D.2.** A (timed) transition system \( \mathcal{A} \) is a quadruple \((S, L, \rightarrow, s)\) where

- \( S \) is a set of states;
- \( L \) is a set of labels;
- \( \rightarrow \subseteq (S \times L \times S) \cup (S \times S) \) is a transition relation;
- \( s \in S \) is the initial state.

Elements \((s', l, s'') \in \rightarrow\) are generally written as \( s' \xrightarrow{l} s'' \). Elements \((s', s'') \in \rightarrow\) are called idle steps and are generally written as \( s' \xrightarrow{\varepsilon} s'' \).

D.3 Transition system (specific)

**Definition D.3.** Let \( E \) be a well-formed Specification and \( \text{Sorts} \supseteq \text{Sig}(E), \text{Sort} \) a set of Names. Let \( A = (\{D_S | S \in \text{Sorts}\}, \Gamma) \) be a model of \( E \). Let \( p \) be a Process-term that is SSC wrt. \( \text{Sig}(E, A) \) and \( \emptyset \). The meaning of \( p \) in the context of \( E \) and \( A \) is the timed transition system \( \mathcal{A}(p, E, A) \) defined by

\[(S, L, \rightarrow, s)\]

where

- \( S \overset{\text{def}}{=} (\text{Term} \cup \{\sqrt{\cdot}\}) \times D_{\text{Time}} \) where \( \text{Term} \) are the Process-terms that are SSC wrt. \( \text{Sig}(E, A) \) and \( \emptyset \) and \( \sqrt{\cdot} \) is a special termination symbol;
- \( L \overset{\text{def}}{=} \{\tau\} \cup \{n(c_u, \ldots, c_u_m) \mid m \geq 0, n \in \text{Sig}(E).\text{Act} \text{ and } u_i \in D_S \text{ for some } S \in \text{Sorts}\};\)
REFERENCES

\[ \langle p, t \rangle \xrightarrow{\text{s}} \langle p, t' \rangle \text{ iff } U(t', p) \text{ and not } t' \leq A t \]

Table 8: Rule for time passing

- \( s \) is the transition relation that contains exactly all transitions provable using the rules in Tables 8, 9, 10 and 11. In these tables \( p, q, p' \) and \( q' \) range over Process-terms that are SSC wrt. \( \text{Sig}(E) \) and \( \emptyset \); \( p, q \) may be either equal to \( \sqrt{ } \), or can be a Process-term which is SSC wrt. to \( \text{Sig}(E) \) and \( \emptyset \). \( P \) is a Process-term which is SSC wrt. \( \text{Sig}(E) \) and some set of variables \( \mathcal{V} \). The variables \( t, t' \in D_{\text{time}}, l \) and \( l' \) range over the set \( L \) of labels, \( n, n_i \) are Names, \( m \geq 0 \) unless stated otherwise, \( k \geq 1 \) and \( u, u_1, \ldots, u_m \) are Data-terms with \( S \in \text{Sorts} \). In Table 11, the conditions below a pair of rules apply to both rules.

**Definition D.4.** Let \( E \) be a well-formed Specification containing an Initial-Declaration init \( p \) and let \( \text{Sorts} \supseteq \text{Sig}(E) \). \( \text{Sorts} \) be a set of Names. Let \( A = (\{ D_s | S \in \text{Sorts} \}, I ) \) be a model of \( E \). The meaning of \( E \) in the context of \( A \) is the timed transition system \( A(p, E, A) \).

References


Table 9: Rules for the basic operators for timed \( \mu \text{CRL} \) (\( m \geq 1 \))
Table 10: Rules for the parallel operators of timed \(\mu\)CRL (\(\pi_1 | \pi_2 = n \in \text{Sig}(E)\). Comm*)
\begin{table}
\[
\begin{array}{c}
\frac{\langle p, t \rangle \xrightarrow{t} \langle p', t \rangle}{\langle \tau_{(m, \ldots, n_o)} p, t \rangle \xrightarrow{t} \langle \tau_{(m, \ldots, n_o)} p', t \rangle} & \frac{\langle p, t \rangle \xrightarrow{t} \langle \sqrt{t} \rangle}{\langle \tau_{(m, \ldots, n_o)} p, t \rangle \xrightarrow{t} \langle \sqrt{t} \rangle} \\
\text{if } l \equiv n(u_1, \ldots, u_n) \text{ then } n \not\equiv n_i \text{ for all } 1 \leq i \leq k & \text{if } l \equiv n(u_1, \ldots, u_n) \text{ then } n' \equiv n'(u_1, \ldots, u_n). \text{ Otherwise } l \equiv l' \\
\frac{\langle p, t \rangle \xrightarrow{n(u_1, \ldots, u_n)} \langle p', t \rangle}{\langle \tau_{(m, \ldots, n_o)} p, t \rangle \xrightarrow{n(u_1, \ldots, u_n)} \langle \tau_{(m, \ldots, n_o)} p', t \rangle} & \frac{\langle p, t \rangle \xrightarrow{n(u_1, \ldots, u_n)} \langle \sqrt{t} \rangle}{\langle \tau_{(m, \ldots, n_o)} p, t \rangle \xrightarrow{n(u_1, \ldots, u_n)} \langle \sqrt{t} \rangle} \\
\text{for all } 1 \leq i \leq k & \text{for all } 1 \leq i \leq k \\
\end{array}
\]
\end{table}

Table 11: Rules for hiding, encapsulation, renaming and time for timed $\mu$CRL
REFERENCES


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