Part 3: Cooperative Verification by Reducer-Based Construction of Conditional Verifiers

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Wehrheim

LMU Munich, Germany







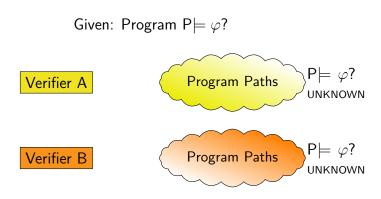


Vision

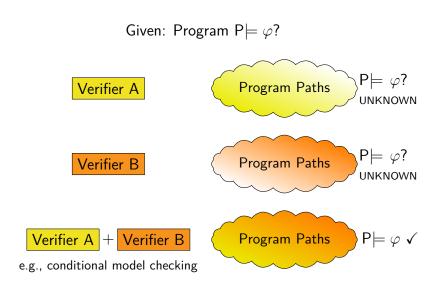
I have a dream ...

- ... that one day, all tools for formal methods work together to solve hard verification problems and make our world safer and more secure.
- ... that one day, model checkers and theorem provers can be integrated into the software-development process as seamless as unit testing today.
- ... that one day, model checkers, theorem provers, SMT solvers, and testers use common interfaces for interaction and composition.

Facing Hard Verification Tasks

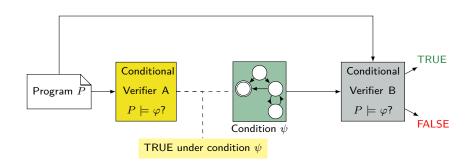


Facing Hard Verification Tasks

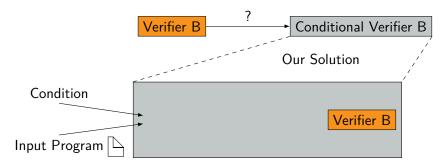


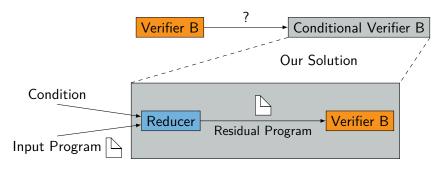
Conditional Model Checking

[Beyer/Henzinger/Keremoglu/Wendler FSE'12]



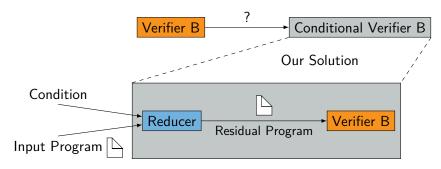
Verifier B ? Conditional Verifier B





Reducer (preprocessor)

- Builds standard input (C program)
- Representing a subset of paths
- Contains at least all non-verified paths



Reducer (preprocessor)

- Builds standard input (C program)
- Representing a subset of paths
- Contains at least all non-verified paths
- + Verifier-unspecific approach
- + Many conditional verifiers possible

Example Program and Condition

```
Program

0: if (notThursday)

1: discount=day%7;
else

2: discount=5;
3: assert(0<=discount<7);
4:

Program

onotThursday
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Example Program and Condition

Verifier A only proofs else branch

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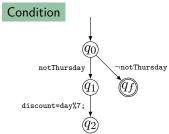
2: discount=5;

3: assert(0<=discount<7);

4:

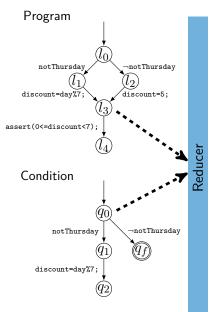
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```

Verifier A only proofs else branch

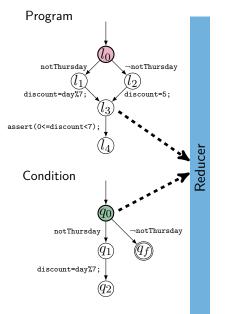


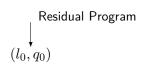
 \neg notThursday

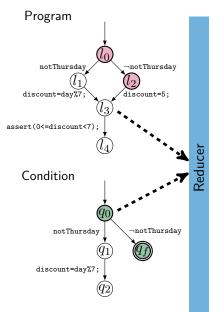
discount=5;

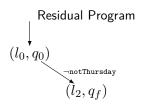


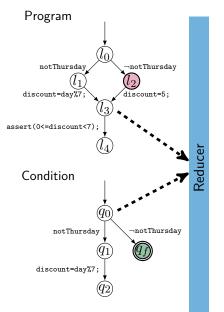
Residual Program

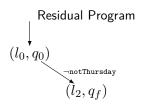


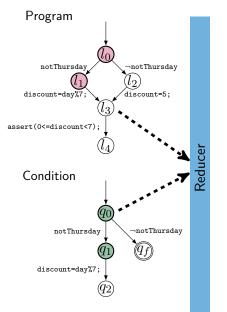


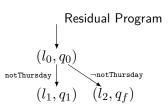


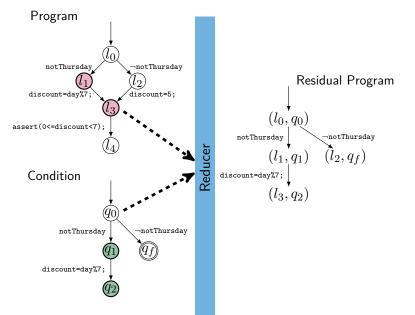


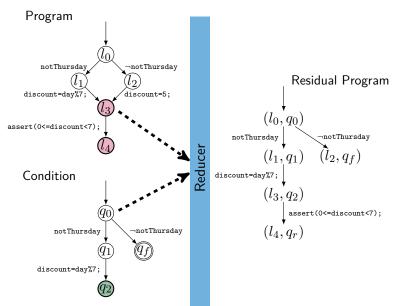




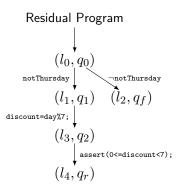




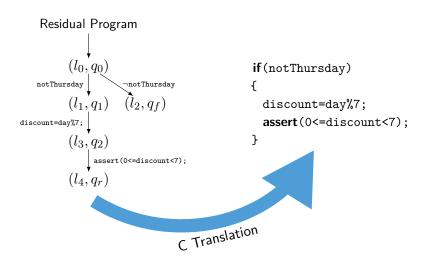




Reducer: C Transformation

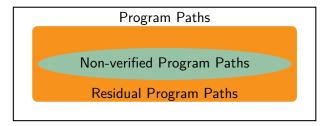


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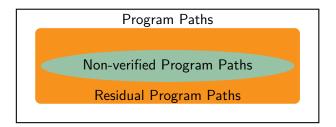
Reducer: Soundness

Residual Condition



Reducer: Soundness

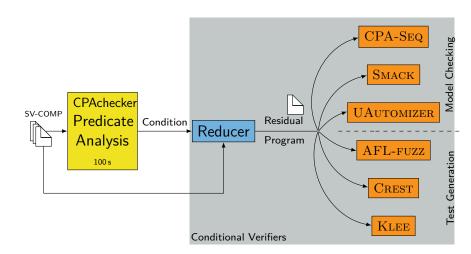
Residual Condition



Theorem

Presented reducer fulfills residual condition.

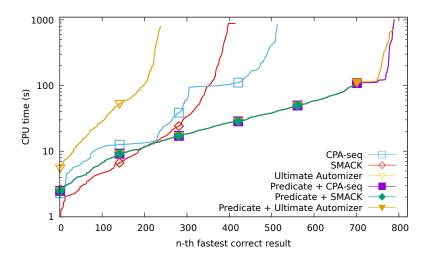
Evaluation Setup



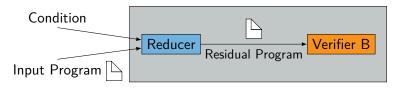
Small Extract of Results

						PREDICATE		PREDICATE	
		CPA-SEQ		UAUTOMIZER		+Reducer		+Reducer	
						+CPA-SEQ		+UAUTOMIZER	
Task	R	S	t(s)	S	t(s)	S	t(s)	S	t(s)
P15l01	Т	Х	910	X	900	1	120	1	130
flood4	Т	X	910	X	910	1	450	X	1100
newt3_6	F	X	950	X	490	X	910	1	260
P07l38	Т	X	950	X	910	X	1100	1	470

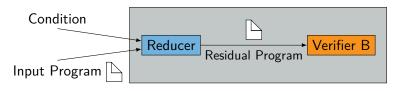
Effectiveness on Hard Tasks



Template-based conditional verifier construction

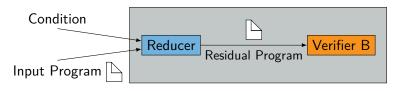


Template-based conditional verifier construction



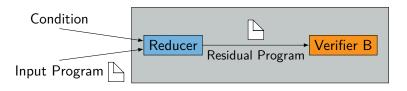
- One Reducer
 - Proven sound
 - Used in many conditional verifiers

Template-based conditional verifier construction



- One Reducer
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 - Used in many conditional verifiers
- Effective on hard tasks for verifiers and test tools

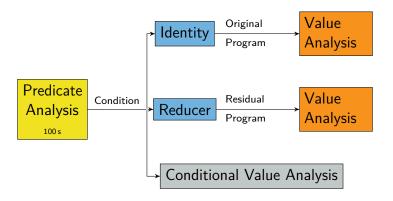
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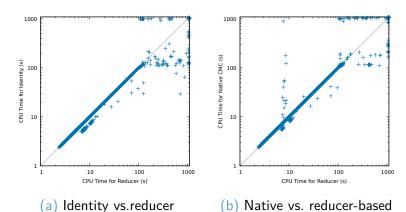
- One Reducer
 - Proven sound
 - Used in many conditional verifiers
- Effective on hard tasks for verifiers and test tools
- Future Work
 - More reducers
 - Using conditions from other tools

Dirk Beyer

Comparison Setup



Comparison Results



References I



D. Beyer, T. A. Henzinger, M. E. Keremoglu, and P. Wendler. Conditional Model Checking: A Technique to Pass Information Between Verifiers. In *Proc. FSE*. ACM, 2012.