

SECCSL

Security Concurrent Separation Logic

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Motivation: Concurrent Information Flow

```
// global shared record
struct record { bool classified; int data; };
struct record * rec = ...;

// memory-mapped IO device register
volatile int * const OUTPUT_REG = ...;
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Correctness Proof

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| Memory Safety | → | Separation Logic |
| + Mutual Exclusion | → | Concurrent SL |

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| Memory Safety | → | Separation Logic |
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| + No Information Leak | → | SEC CSL |

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Invariants in SECCSL

$\exists c. \text{rec->classified} \mapsto c$

$\exists d. \text{rec->data} \mapsto d$

$\exists v. \text{OUTPUT_REG} \longleftarrow v$

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Invariants in SECCSL

$$\exists c. \text{rec->classified} \mapsto c \quad \wedge \quad c :: \text{low}$$

$$\exists d. \text{rec->data} \mapsto d \quad \wedge \quad d :: (c ? \text{high} : \text{low})$$

$$\exists v. \text{OUTPUT_REG} \xrightarrow{\text{low}} v$$

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Verification in SECC

```
./secc examples/example.c
thread1 ... success ♥ (time: 12ms)
thread2 ... success ♥ (time: 5ms)
```

Contribution

Goal: Proof system

- ▶ for low-level concurrent systems code
- ▶ for expressive, value-dependent information flow
- ▶ with good automation

Results

- ▶ SECCSL = CSL \uplus Non-Interference
- ▶ Soundness mechanized in Isabelle/HOL
- ▶ Prototype Verifier SECC for C
- ▶ <https://covern.org/secc>



SECCSL Assertions

- ▶ Security-Labels $\ell \in \{\text{low}, \dots, \text{high}\}$
- ▶ $P ::= e :: e_\ell \mid e_p \xrightarrow{e_\ell} e_v \mid \varphi \Rightarrow P \mid P \star Q \mid \exists x. P$

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- ▶ Value classification $e :: e_\ell$ (information sources)
 - ▶ $e :: \text{low}$ *data e is public*
 - ▶ $e :: (c ? \text{high} : \text{low})$ *data-dependent classification*

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- Label e_ℓ is an expression, not a type
- ▶ Location sensitivity $e_p \xrightarrow{e_\ell} e_v$ (information sinks)
 - ▶ $e_p \xrightarrow{\text{low}} e_v$ *location e_p is attacker-observable*
 - ▶ $e_p \xrightarrow{e_\ell} e_v$ *implies $e_p :: e_\ell$ and $e_v :: e_\ell$*

Proof rules keep e_ℓ tied to e_p

Non-Interference via Relational Semantics

- ▶ Security: Attacker can distinguish states by `low` data only
- ▶ Non-interference: `low`-equivalence is a bisimulation

$$\begin{array}{ccccccc} (s_0, h_0) & \longrightarrow & (s_1, h_1) & \longrightarrow & (s_2, h_2) & \longrightarrow & \cdots \\ | \equiv_{\text{low}} & & | \equiv_{\text{low}} & & | \equiv_{\text{low}} & & \\ (s'_0, h'_0) & \longrightarrow & (s'_1, h'_1) & \longrightarrow & (s'_2, h'_2) & \longrightarrow & \cdots \end{array}$$

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- ▶ Relational Semantics, notably

$$\begin{array}{lll} (s, h), (s', h') \models e :: \text{low} & \iff & [[e]]_s = [[e]]_{s'} \\ (s, h), (s', h') \models p \xrightarrow{\text{low}} a & \implies & [[a]]_s = [[a]]_{s'} \end{array}$$

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- ▶ Bisimulation: for $\{P\} \subset \{Q\}$ let $(s, h) \equiv_{\text{low}} (s', h')$
 $\iff \exists R. \text{lows}(P, s) \subseteq \text{lows}(R, s) \text{ and } (s, h), (s', h') \models R$

SECCSL = SEC \oplus CSL

$$\frac{}{\{y \mapsto a\} \ x = *y \ \{x = a \wedge y \mapsto a\}} \text{ LOAD}$$

$$\frac{}{\{x \mapsto a\} \ *x = b \ \{x \mapsto b\}} \text{ STORE}$$

$$\frac{\{b \wedge P\} \ c_1 \ \{Q\} \quad \{\neg b \wedge P\} \ c_2 \ \{Q\}}{\{P\} \ \text{if}(b) \ c_1 \ \text{else} \ c_2 \ \{Q\}} \text{ IF}$$

$$\frac{\{ \quad b \wedge P\} \ c \ \{ \quad P\}}{\{ \quad P\} \ \text{while}(b) \ c \ \{\neg b \wedge P\}} \text{ WHILE}$$

SECCSL = SEC \uplus CSL

$$\frac{}{\{y \xrightarrow{\ell} a\} \ x = *y \quad \{x = a \wedge y \xrightarrow{\ell} a\}} \text{LOAD}$$

$$\frac{}{\{x :: \ell \wedge b :: \ell \wedge x \xrightarrow{\ell} a\} \ *x = b \quad \{x \xrightarrow{\ell} b\}} \text{STORE (secure)}$$

$$\frac{\{b \wedge P\} \ c_1 \ \{Q\} \quad \{\neg b \wedge P\} \ c_2 \ \{Q\}}{\{b :: \text{low} \wedge P\} \ \text{if}(b) \ c_1 \ \text{else} \ c_2 \ \{Q\}} \text{ IF (timing sensitive)}$$

$$\frac{\{b :: \text{low} \wedge b \wedge P\} \ c \ \{b :: \text{low} \wedge P\}}{\{b :: \text{low} \wedge P\} \ \text{while}(b) \ c \ \{\neg b \wedge P\}} \text{ WHILE (timing sensitive)}$$

Thanks for your attention

SECCSL: a logic for

- ▶ low-level concurrent systems code
- ▶ expressive, value-dependent information flow

SECC: a prototype automated verifier for SECCSL

Check it out: <https://covern.org/secc>

Backup

Related Work

- ▶ Taint-tracking, type systems: not semantic, does not accept $\text{if}(x == x) \{ \dots \}$ when $x :: \text{high}$
- ▶ Banerjee, Naumann (S&P 2008): $A(e) \iff e :: \text{low}$
- ▶ Costanzo & Shao (POST 2014): Labels attached to semantic values, fails to validate $e :: \ell \Rightarrow f(e) :: \ell$ and $(e_1 = e_2) \Rightarrow (e_1 :: \ell \iff e_2 :: \ell)$
- ▶ Karbyshev et al (POST 2018): timing insensitive
- ▶ Eilers et al (ESOP 2018): self-composition (concurrency?)
- ▶ Vafeiadis (MFPS 2011): soundness proof has same structure
- ▶ Murray et al (EuroS&P 2018): no pointers, lack of integration between functional and security proofs

Invariants in SecC (concrete syntax)

```
void lock(struct mutex * m);
  -(ensures exists int v.
    OUTPUT_REG |->[low] v)
  -(ensures exists int c, int d.
    &rec->is_classified |->[low] c &&
    &rec->data |->d &&
    d :: (c ? high : low))

void unlock(struct mutex * m);
  -(requires exists int v. OUTPUT_REG |->[low] v)
  -(requires exists int c, int d.
    &rec->is_classified |->[low] c &&
    &rec->data |->d &&
    d :: (c ? high : low))
```

Logic Case Splits

$$\frac{\{\varphi \wedge P\} \quad c \quad \{Q\} \quad \{\neg\varphi \wedge P\} \quad c \quad \{Q\}}{\{\varphi :: \text{low} \wedge P\} \quad c \quad \{Q\}} \text{ SPLIT}$$

- ▶ Why? $(s, s') \models \varphi \iff s \models \phi \text{ and } s' \models \phi$
- ▶ (Relational semantics can represent 2 of 4 cases only)

Secure Entailment (for CONSEQ)

$P \xrightarrow{\text{low}} Q$ holds iff

- ▶ $(s, h), (s', h') \models P$ implies
 $(s, h), (s', h') \models Q$ for all s, h and s', h' , and
- ▶ $\text{lows}(P, s) \subseteq \text{lows}(Q, s)$ for all s

Observable Locations

$$\text{lows}_\ell(e :: e_\ell, s) = \emptyset$$

$$\text{lows}_\ell(P \star Q, s) = \text{lows}_\ell(P, s) \cup \text{lows}_\ell(Q, s)$$

$$\text{lows}_\ell(e_p \xrightarrow{e_\ell} e_v, s) = \begin{cases} \{\llbracket e_p \rrbracket_s\}, & \llbracket e_\ell \rrbracket_s \sqsubseteq \ell \\ \emptyset, & \text{otherwise} \end{cases}$$

Security Property

- ▶ $\text{secure}_\ell^0(P_1, c_1, Q)$ always.
- ▶ $\text{secure}_\ell^{n+1}(P_1, c_1, Q)$ iff for all pairs of states $(s_1, h_1), (s'_1, h'_1)$, frames F , lock sets L_1 with $(s_1, h_1), (s'_1, h'_1) \models_\ell P_1 \star F \star \text{invs}(L_1)$ where $(\text{run } c_1, L_1, s_1, h_1) \xrightarrow{\sigma} k$ and $(\text{run } c_1, L_1, s'_1, h'_1) \xrightarrow{\sigma} k'$ with the same (deterministic) schedule σ
there exists P_2 and a pair of successor states with either of
 - ▶ $k = (\text{stop } L_2, s_2, h_2)$ and $k' = (\text{stop } L_2, s'_2, h'_2)$ and $P_2 = Q$
 - ▶ $k = (\text{run } c_2, L_2, s_2, h_2)$ and $k' = (\text{run } c_2, L_2, s'_2, h'_2)$ with $\text{secure}_\ell^n(P_2, c_2, Q)$such that in both cases
 - ▶ $(s_2, h_2), (s'_2, h'_2) \models_\ell P_2 \star F \star \text{invs}(L_2)$ and
 - ▶ $\text{lows}_\ell(P_1 \star \text{invs}(L_1), s_1) \subseteq \text{lows}_\ell(P_2 \star \text{invs}(L_2), s_2)$